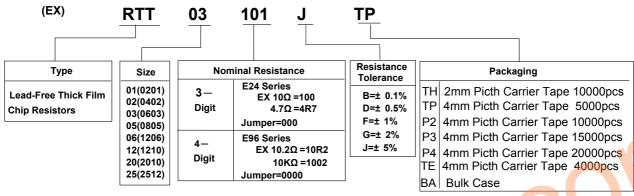
Thick Film Chip Resistors Product Specification

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1 Scope

This specification is applicable to lead and halogen free RTT series thick film chip resistors.

2 Explanation Of Part Numbers



3 General Specifications

3.1 Resistance Range: \geq 1 Ω

0. 1	6.1 Modelation Manage. = 1 42												
Туре	Rated Power at 70℃			Norking Overload	T.C.R.		Resistan	ce Range	10	Jum Rai Cur	ted	Resis	nper stance lue
		Voltage	Voltage	(bbm/℃)	B(± 0.1%) E-24 \ E-96	D(± 0.5%) E-24 \ E-96	F(± 1%) E-24 • E-96	G(± 2%) \ J(± 5%) E-24	J (± 5%)	F (<u>±</u> 1%)	J (± 5%)	F (± 1%)	
RTT01 (0201)	1 w	25V	50V	-200 +400		$1\Omega \leq R < 10\Omega$	$1\Omega \leq R < 10\Omega$	$1\Omega \leq R < 10\Omega$	0.5A	0.5A	50m Ω	$35m\Omega$ MAX.	
(0201)	20			± 200		$10\Omega \le R \le 10M\Omega$	$10\Omega \le R \le 10M\Omega$	$10\Omega \le R \le 10M\Omega$			IVI/A/X.	Wir UX.	
RTT02	1			± 100	$100\Omega \le R \le 1M\Omega$	10Ω ≤ R ≤ 1MΩ	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega \leq R \leq 1M\Omega$			50m Ω	20m Ω	
(0402)	16 W	50V	100V	± 200			$\begin{array}{l} 1\Omega \! \leq \! R \! < \! 10\Omega \\ 1M \! < \! R \! \leq \! 10M\Omega \end{array}$	$\begin{array}{l} 1\Omega\!\leq\!R\!<\!10\Omega\\ 1M\!<\!R\!\leq\!20M\Omega \end{array}$	1A	1.5A		MAX.	
RTT03	1			± 100	$100\Omega \le R \le 1M\Omega$	$10\Omega{\leq}R{\leq}1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$				50m Ω	20m Ω	
(0603)	10 w	75V	150V	± 200		$1\Omega \leq R < 10\Omega$	$\begin{array}{c} 1\Omega \leqq R < 10\Omega \\ 1M < R \leqq 10M\Omega \end{array}$	1Ω≦R≦20MΩ	1A	2A	MAX.	MAX.	
RTT05	1	150V	300V	± 100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$		2A 2.5A		50mΩ 20m	20m Ω	
(0805)	8 w	1500	300 V	± 200		$1\Omega \leq R < 10\Omega$	$\begin{array}{c} 1\Omega \leqq R < 10\Omega \\ 1M < R \leqq 10M\Omega \end{array}$	1Ω≦R≦20MΩ		2.5A	MAX.	MAX.	
RTT06	1			± 100	$100\Omega \leq R \leq 1M\Omega$	$10\Omega \le R \le 1M\Omega$	$10\Omega \le R \le 1M\Omega$			3.5A	50m Ω MAX.	$20m\Omega$ MAX.	
(1206)	4 W	200V	400V	± 200		$1\Omega \leq R < 10\Omega$	$\begin{array}{l} 1\Omega \leqq R < 10\Omega \\ 1M < R \leqq 10M\Omega \end{array}$	1Ω≦R≦20MΩ	2A				
				± 100	$100\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$						
RTT12 (1210)	1 w	200V	400V	± 200			1M <r≦10mω< td=""><td>10Ω≦R≦20MΩ</td><td>2A</td><td>4A</td><td>$\begin{array}{c} \text{50m}\Omega\\ \text{MAX}. \end{array}$</td><td>$\begin{array}{c} \text{20m}\Omega\\ \text{MAX}. \end{array}$</td></r≦10mω<>	10Ω≦R≦20MΩ	2A	4A	$\begin{array}{c} \text{50m}\Omega\\ \text{MAX}. \end{array}$	$\begin{array}{c} \text{20m}\Omega\\ \text{MAX}. \end{array}$	
				± 400			$1\Omega \leq R < 10\Omega$	1Ω≦R<10Ω					
RTT20	3			± 100	$100\Omega \leq R \leq 1M\Omega$	$10\Omega \le R \le 1M\Omega$	$10\Omega\!\leq\!R\!\leq\!1M\Omega$				500	20	
(2010)	- 4 w	200V	400V	± 200				$10\Omega \le R \le 10M\Omega$	2A	5A	50m Ω	$20m\Omega$ MAX.	
(=0.0)	4			± 400			$1\Omega \leq R < 10\Omega$	$1\Omega \leq R < 10\Omega$					
				± 100	$100\Omega \le R \le 1M\Omega$	$10\Omega \le R \le 1M\Omega$	$10\Omega \le R \le 1M\Omega$						
RTT25 (2512)	1W	200V	400V	± 200				$10\Omega \le R \le 10M\Omega$	2A	7A	50m Ω	$20m\Omega$ MAX.	
(2312)				± 400			$1\Omega \leq R < 10\Omega$	$1\Omega \leq R < 10\Omega$			WIAA.	WIZZZ.	
Operat	ting Tem	peratur	e Range			-55°C ~ +15	5℃ (0201:-	-55°C ~ +125°C)				
operating reinperature realige			•			•							

Approved





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3.2 Resistance Range: <1 Ω

Туре	Rated Power at 70℃	Max. Rated Current	Max. Overload Current	T.C.R (ppm / ℃)	Resistance Range F(± 1%) \ G(± 2%) \ J((± 5%) E-24 \ E-96
				± 600	E-24 \ E-96 80 ≤R<200 mΩ
DTTOO			-	± 300 ± 300	30 ≦R<200 mΩ 200≦R<400 mΩ
RTT02 (0402)	1/16W	0.88A	2.2A	± 250	$200 \le R < 400 \text{ m}\Omega$ $400 \le R < 600 \text{ m}\Omega$
(0402)			-	± 200	400 ≦ R < 600 mΩ 600 ≤ R < 1000 mΩ
				± 1500	10≦R<1000 IIIΩ 10≦R<37 mΩ
			-	± 1200	$37 \le R < 60 \text{ m}\Omega$
DTTAG					37 ≦ R < 60 mΩ 60 ≤ R < 100 mΩ
RTT03 (0603)	1/10W	3.16A	7.91A	± 600 ± 300	
(0003)			-		100 ≦R<200 mΩ
				± 600	200 ≦R<500 mΩ
				± 400	500 ≦R<1000 mΩ
				± 1500	$10 \le R < 19 \text{ m}\Omega$
RTT05	1/8/// 1 3534		0.004	± 1200	$19 \le R < 33 \text{ m}\Omega$
(0805)			8.82A	± 800	$33 \le R < 50 \text{ m}\Omega$
, ,				± 600	50 ≦R<100 mΩ
				± 200	100 ≦R<1000 mΩ
				± 1500	10 ≤R<19 mΩ
RTT06	4 (0) 4 (14.42A	± 1200	$19 \leq R < 25 \mathrm{m}\Omega$
(1206)	1/3W	5.77A		± 1000	$25 \leq R < 50 \text{ m}\Omega$
, ,				± 600	50 ≦R<100 mΩ
				± 200	100 ≦R<1000 mΩ
				± 1500	10 ≦R<19 mΩ
RTT12				± 1000	19 \leq R < 25 m Ω
(1210)	1/2W	7.07A	17.67A	± 700	25 \leq R < 50 m Ω
(,				± 400	50 ≦R<100 mΩ
				± 200	100 \leq R<1000 mΩ
				± 1500	10 ≦R<19 mΩ
RTT20				± 1200	19 \leq R < 25 m Ω
(2010)	3/4W	8.66A	21.65A	± 900	$25 \leq R < 50 \text{ m}\Omega$
(2010)				± 500	$50 \leq R < 100 \text{ m}\Omega$
				± 200	100 \leq R<1000 mΩ
				± 1500	10 \leq R<19 mΩ
RTT25				± 1200	19 \leq R < 25 m Ω
(2512)	1 W	10A	25A	± 900	25 ≤R<50 m Ω
(2312)				± 500	50 ≦R<100 mΩ
				± 200	100 ≤R<1000 m Ω
	Operating 7	Temperature R	ange		_55°C ~ +155°C

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3.3 Power Derating Curve:

Type	RTT01 (0201)	Other
Operating Temperature Range	-55°C ~ +125°C	−55°C ~ +155°C
Explain	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.	For resistors operated in ambient temperatures above 70°C, power rating shall be derated in accordance with figure below.
Figure	100 70 (%) 80 60 60 40 125 125 125 125 125 125 125 125 125 125	100

3.4 Voltage Rating or Current Rating:

3.4.1 Resistance Range: $\geq 1 \Omega$

Rated Voltage: The resistor shall have a DC continuous working voltage or a rms. AC continuous working voltage at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$E = \sqrt{R \times P}$$

E= Rated voltage (v)

P= Power rating (w)

R= Nominal resistance(Ω)

3.4.2 Resistance Range: $< 1 \Omega$

Rated Current: The resistor shall have a DC continuous working current or a rms. AC continuous working current at commercial-line frequency and wave form corresponding to the power rating, as determined from the following:

$$I = \sqrt{P/R}$$

I= Rated current (A)

P= Power rating (w)

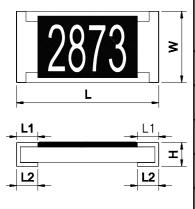
R= Nominal resistance(Ω)

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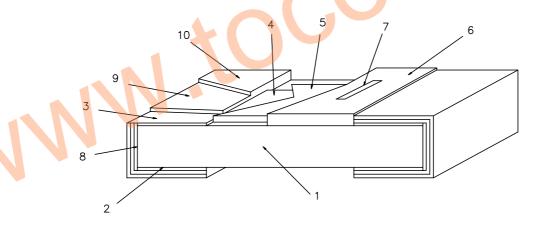
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4 Dimensions



	Unit : n							
Dir TYPE	nensions Size Code	L	w	Н	L1	L2		
RTT01	0201	0.60± 0.03	0.30± 0.03	0.23± 0.03	0.15± 0.05	0.15± 0.05		
RTT02	0402	1.00± 0.10	0.50± 0.05	0.30± 0.05	0.20± 0.10	0.25± 0.10		
RTT03	0603	1.60± 0.10	0.80± 0.10	0.45± 0.10	0.30± 0.15	0.30± 0.15		
RTT05	0805	2.00± 0.10	1.25± 0.10	0.50± 0.10	0.35± 0.20	0.35± 0.15		
RTT06	1206	3.05± 0.10	1.55± 0.10	0.55 ^{+0.10} _{-0.05}	0.45± 0.20	0.35± 0.15		
RTT12	1210	3.05± 0.10	2.55± 0.10	0.55± 0.10	0.50± 0.20	0.50± 0.20		
RTT20	2010	5.00± 0.20	2.50± 0.20	0.55± 0.10	0.60± 0.20	0.60± 0.20		
RTT25	2512	6.30± 0.20	3.20± 0.20	0.55± 0.10	0.60± 0.20	0.60± 0.20		

5 Structure Graph



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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6 Reliability Test 6.1 Electrical Performance Test

U. I LIEUI	icai Feriorinarice Test	l		
ITEM	Conditions	Specifications		
11 -111	Conditions	Resistors	Jumper	
Temperature	TCR (ppm / °C) = $\frac{(R2-R1)}{R1(T2-T1)} \times 10^6$	Refer to item 3. general specifications	NA	
Coefficient of	· /			
Resistance	R1: Resistance at room temperature R2: Resistance at -55 $^{\circ}$ C or +125 $^{\circ}$ C			
	T1: Room temperature			
	T2: Temperature -55°C or +125°C			
	Refer to JIS-C5201-1 4.8			
Short Time	Applied 2.5 times rated voltage for 5 seconds and release	1.Resistance Range:≧1Ω	Refer to item 3.	
Overload	the load for about 30 minutes, then measure its resistance	= \	general	
	variance rate. (Rated voltage refer to item 3. general	2% \ 5%: \pm (2.0% + 0.10 Ω) 2.Resistance Range:<1 Ω	specifications	
	specifications)	1% \ 2% \ 5%:± $(2.0\% + 0.001\Omega)$		
	Jumper : Applied Maximum overload current	No evidence of mechanical damage	e.	
	Type RTT01 RTT02 RTT03 RTT05 RTT06 RTT12 RTT20 RTT25 Jumper (0201) (0402) (0603) (0805) (1206) (1210) (2010) (2512)	l l l l l l l l l l l l l l l l l l l	_ 4 1	
	± 5% 1.25A 2.5A 2.5A 5A 5A 5A 5A 5A			
	± 1% 1.25A 3.75A 5A 6.25A 8.75A 10A 12.5A 17.5A			
	Refer to JIS-C5201-1 4.13			
Insulation	Put the resistor in the fixture, add 100 VDC in + ,- terminal			
Resistance	for 60 sec then measured the insulation resistance between			
	electrodes and insulating enclosure or between electrodes			
	and base material.			
	Refer to JIS-C5201-1 4.6			
	Insulating plate Metal block measuring			
	Point A Metal plate measuring point B			
	Pressurizing by spring			
	Base material Specimen Pressurizing by spring Insulating enclosure surface R0.5mm			
Dielectric	Put the resistor in the fixture, add VAC (see SPEC below)	No short or burned on the appeara	ince.	
Withstand	in +,- terminal for.	The chort of barried on the appeara		
Voltage	RTT05 \ \ 06 \ \ 12 \ \ 20 \ \ 25 apply 500 VAC 1 minute.			
Voltage	RTT01 \ 02 \ 03 apply 300 VAC 1 minute.			
	Refer to JIS-C5201-1 4.7			
Intermittent	Put the tested resistor in chamber under temperature 25±	1.Resistance Range:≧1Ω	Refer to item	
Overload	$2^{\circ}\!$	$\pm (5.0\% + 0.10\Omega)$	3. general	
	sec off, 10000 +400 test cycles, then it be left at no-load for	2.Resistance Range:<1Ω	specifications	
	1 hour , then measure its resistance variance rate.	$\pm (5.0\% + 0.001\Omega)$		
	Jumper : Applied Maximum overload current			
	Type RTT01 RTT02 RTT03 RTT05 RTT06 RTT12 RTT20 RTT25			
	Jumper (0201) (0402) (0603) (0805) (1206) (1210) (2010) (2512) ± 5% 1.25A 2.5A 2.5A 5A 5A 5A 5A			
	± 1% 1.25A 2.5A 2.5A 5A 5A 5A 5A 5A 5A 17.5A			
	Refer to JIS-C5201-1 4.13			
Noise Level	Refer to JIS-C5201-1 4.12	Resistance Noise	NA	
1		R $<$ 100 Ω \leq -10db(0.32 uV/V)	'	
		$100\Omega \le R < 1K\Omega \qquad \le 0db(1.0 \text{ uV/V})$		
		$\begin{array}{c c} 1K\Omega & \leq R < 10K\Omega & \leq 10db(3.2 \text{ uV/V}) \\ 10K\Omega & \leq R < 100K\Omega & \leq 15db(5.6 \text{ uV/V}) \end{array}$		
		$\frac{1000 \Omega}{100 \text{K}\Omega} \stackrel{\leq}{\leq} \text{R} < 100 \text{K}\Omega \stackrel{\leq}{\leq} 13 \text{db}(3.5 \text{ dV/V})$		
		1M Ω ≤R ≤ 30db(32 uV/V)		

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6.2 Mechanical Performance Test

o.∠ Mecha	anical Perforr	mance rest			
ITEM	Specifications Specifications				
I I EIVI	Conditions		Resistors	Jumper	
	pushing 10N { 1. 10 sec. 1.RTT02 \ RTT0 2.RTT05 \ 06 \	12、20、25: probe R0.5	1.Resistance Range: ≥ 1Ω ± (1.0%+0.05Ω) 2.Resistance Range:<1Ω ± (1.0%+0.001Ω) No evidence of mechanical da	Refer to item 3. general specifications mage.	
Torrainal	Refer to JIS-C52		No side conductive peel off.	anical	
Terminal Strength	5N pushir sec. (RTT Test 2 : The resi add force	stor mounted on the board slowly on the sample rear until the ermination is breakdown.	Test 1 : No evidence of mechadamage. Test 2 : RTT01≧3N Other Type≧5N	ariical	
	alcohol of 20~25		1.Resistance Range: ≥ 1 Ω Type RTT01 Other △R% ± (1.0%+0.05Ω)± (0.5%+0.05Ω 2.Resistance Range: <1 Ω ± (1.0%+0.001 Ω) No evidence of mechanical da overcoating and Sn layer by le	image, no G2	
Solderability	Preconditioning		1.Test item 1:	<u>J-</u>	
·	Put the tested re temperature of 1 pressure of 1.22. Then after left th temperature for 2 Test method: Test method: Test item 1 (so The resistor be temperature 2 is left as place solder area. Test item 2 (w Add flux into re wetting balance below, then me time changed. Testing conditions Solder temperature Immersion speed	older pot test): e immersed into solder pot in 35± 5°C for 2 sec, then the resistor d under microscope to observed its etting balance method): esistors, then put resistor into e machine, refer to condition as ust be measured and recorded its for wetting balance method with solder pot Condition 235± 2°C 1 to 5 mm/s	2. Test item 2: Zero cross time within 3 sec	onds.	
	Immersion depth	0.10 mm			
	Immersion angle				
	Refer to JIS-C52				

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		Specifications	\neg
ITEM	Conditions	·	
		Resistors Jumper	
	OT 1 11 14 75 75 11 11	I D I I D I I D	4
	⊚Test method 1 (Reflow test):	1.Resistance Range:≧1Ω Refer to iter	n
Soldering	The tested resistor should be subject in the		20
Heat	following procedure, and after finish each st		15
	should be left for a duration of 2 hours or lor	· · · · · · · · · · · · · · · · · · ·	
	a temperature of 30° C or lower and a humid 70% RH or lower.	lity of No evidence of electrode damage. No side conductive peel off.	
	Step Procedure Environmental test condi		
	Resistance		
	1 Room temperature		
	2 Baking 125°C → 24 hours		
	3 Humidification 85°C ⋅ 85% ⋅ 168 hour		
	Reflow temperature curve a		
	4 Reflow (1) component surface tempera	ature	
	5 Humidification 85°C , 65% , 24 hours	rs	
	Reflow temperature curve a		
	6 Reflow (2) component surface tempera		
	Table 2		
	7 Resistance Room temperature		
	measuring		
	⊚Reflow temperature curve		
	250 Party 360 +5 57		
	1601.200-0		
	230°C Or Higher		
	180°CPre Heating Zone		
	Ten		
	75 150 150°C	\	
	Temperature 1500 1500 90 ± 30 ± 10 5		
	€ 100 30±10 S		
	Soldering Zone	_/	
	50		
	Heating time	\	
	○Component surface temperature		
	Table 1 Description example in specification	ation	
	document (1)		
	Temperature Temperature mea	asured	
	-retaining Peak at the component	t body	
	time :230°C temperature surface during		
	or higher preheating		
	30 seconds 240°C 150 to 160 °C	<u></u>	
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ITENA		0.	-11:41:		Specifications	
ITEM	Conditions				Resistors	Jumper
	Table 2 De		n example ir cument (2)	n specification		
	Temperature		mperature aining time	Temperature measured at the component body surface during preheating		
	220°C or high) seconds			
	230°C or high	_) seconds	150 to 160℃		
	240°C or high Peak	<u>er 5</u>	seconds 245°C	-		
	⊚Test meth	nd 2 (sc				
	The tested re					
				ish each step, it		
				hours or lower at		
			Cor lower a	nd a humidity of		
	70% RH or lo	ower. cedure	TEnvironmer	ntal test condition		
	Res	stance				
		suring		temperature		
		king		, 24 hours		
		dification pot test		5% → 168 hours 3°C → 10 sec		
		aced	85°C , 65% , 24 hours			
		pot test				
		stance suring	Room temperature			
Preheating temperature : 350± 10℃ Electric iron preheating time : 3+1/-0 sec						
	Preheating t					
				aced the iron		
	over 60 min.					
	variance rate					
	By Sony (SS-0 Refer to JIS-C					
Joint Strength	Preconditioning		4.10		Test item 1:	Refer to item
of Solder	Put tested resi		ne apparatus	s of PCT, at a	(1). Vaviance rate on resistance	3. general
	temperature of 105℃, humidity of 100% RH, and			100% RH, and	1.Resistance Range: \geq 1 Ω	specifications
	pressure of 1.22× 10 ⁵ Pa for a duration of 4 hours.		\triangle R%=± (1.0%+0.05 Ω)			
	Then after left the specimen in a temperature for 2 hours or more.		2.Resistance Range:<1 Ω			
	Test method:		$\triangle R\% = \pm (1.0\% + 0.001 \Omega)$			
	⊚Test item 1 (Adhesio	n):		(2).No evidence of mechanical damage	
	A static load us	sing a R	0.5 (0201:R0	0.1) scratch tool	No terminal peel off.	
	shall be applied on the core of the component and in the direction of the arrow and held for 10		'			
I	jiri trie direction	or the a	now and ne	eiu ioi io	I	1 I

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		Chasifications	
ITEM	Conditions	Specifications Resistors	Jumper
	seconds and under load measured its resistance		Jumper
	seconds and under load measured its resistance variance rate. 1.RTT02=10N load 2.Other type=20N load 3.RTT01=5N load Cross-sectional view Scratching jig Test item 2 (Bending Strength): Solder tested resistor on to PC board, add force in the middle down, and under load measured its resistance variance rate. D:RTT02 \ 03 \ 05=5mm RTT01 \ 06 \ 12=3mm RTT20 \ 25=2mm Resistor Resistor Resistor Refer to JIS-C5201-1 4.33 Testing circuit board Chip resistor Chip resistor Refer to Jis-C5201-1 4.33 Test item 3 (Endurance measurement): Put the tested resistor in the chamber under the temperature cycle which shown in table 1 shall be repeated 1000± 4 times consecutively. Then separate follow test item 1 and test item 2 50% condition to test, measured its resistance variance rate.		

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ITEM	Condition	c	Specifications	
I I LIVI	Conditions		Resistors	Jumper
	Table 1 Temperature cycle test condition			
		Testing condition		
	Lowest temperature	-35± 5℃		
	Highest temperature	105± 5℃		
	Temperature-retaining time	15 minutes each		
	By SONY (SS-00254-7)			
Leaching Test The tested resistor be immersed into molten solder of 260± 5°C for 30 seconds. Then the resistor is left as placed under microscope to observed its solder area. By SONY (SS-00254-9)		2.The underlying material (such shall not be visible at the crest of the electrode.		
	The resistor shall be mounted to the supporting terminals on the so The entire frequency range :fr and return to 10 Hz, shall be transplitude :1.5 mm This motion shall be applied for in each 3 mutually perpendicular 6 hr) Refer to JIS-C5201-1 4.22	olid table. om 10 Hz to 55 Hz ansferred in 1 min. or a period of 2 hours	1.Resistance Range : $\geq 1 \Omega$ 0.1% \ 0.5% \ 1%:\(\pmu\) (0.5%+0.05\(\Omega\)) 2% \ 5%:\(\pmu\) (1.0%+0.05\(\Omega\)) 2. :Resistance Range : $<1 \Omega$ 1% \ 2% \ 5%:\(\pmu\) (1.0%+0.001\(\Omega\)) No evidence of mechanical dam	Refer to item 3. general specifications age.

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6.3 Environmental Test

U.U =1.1711	Ommontal root			
ITEM Conditions		Specifications		
			Resistors	Jumper
Resistance to Dry Heat	5°C for 1000 +48/-0 hours. Then leaving the tested resistor in room temperature for 60 minutes , and measure its resistance variance rate. P.S. RTT01 for 125+ 3°C		$ \begin{array}{l} \text{1.Resistance Range:} \geq 1 \Omega \\ \text{0.1\% \cdot 0.5\% \cdot 1\%:} (1.0\% + 0.05 \Omega) \\ \text{2\% \cdot 5\%:} (2.0\% + 0.10 \Omega) \\ \text{2.Resistance Range:} < 1 \Omega \\ \text{1\% \cdot 2\% \cdot 5\%:} (1.0\% + 0.001 \Omega) \\ \text{No evidence of mechanical damage.} \\ \end{array} $	Refer to item 3. general specifications
Thermal Shock	Put the tested resistor in the chamber under the Thermal Shock which shown in the following table shall be repeated 300 times consecutively. Then leaving the tested resistor in the room temperature for 1 hours, and		1.Resistance Range: ≥ 1 Ω 0.1% \cdot 0.5% \cdot 1%: \pm (0.5% + 0.05 Ω) 2% \cdot 5%: \pm (1.0% + 0.05 Ω) 2.Resistance Range:<1 Ω 1% \cdot 2% \cdot 5%: \pm (1.0% + 0.001 Ω) No evidence of mechanical damage.	Refer to item 3. general specifications
		Testing Condition		
	Lowest Temperature	-55± 5°C		
	Highest Temperature	125± 5℃		
	Temperature-retaining time	15 minutes each		
	Refer to MIL-STD 202 Method 107	,		
Loading Life in Moisture	Put the tested resistor in the chaml 40± 2°C, relative humidity 90~95% voltage for 90 minutes on, 30 minu hours. Then leaving the tested resi temperature for 60 minutes, and m variance rate. Refer to JIS-C5201-1 4.24	and load the rated lites off, total 1000 listor in room	1.Resistance Range: ≥ 1 Ω Type RTT01	Refer to item 3. general specifications
	Put the tested resistor in chamber 70± 2°C and load the rated voltage minutes off, total 1000 hours. Then resistor in room temperature for 60 measure its resistance variance rate Refer to JIS-C5201-1 4.25 Put the tested resistor in the chamber the chamber than 25°C. Decreasing the	e for 90 minutes on, 30 n leaving the tested of minutes, and te.	1.Resistance Range: ≥ 1 Ω 0.1% \ 0.5% \ 1%:± (1.0% + 0.05 Ω) 2% \ 5%:± (3.0% + 0.10 Ω) 2.Resistance Range:<1 Ω 1% \ 2% \ 5%:± (2.0% + 0.001 Ω) No evidence of mechanical damage. 1.Resistance Range: ≥ 1 Ω 0.1% \ 0.5% \ 1%:± (0.5% + 0.05 Ω)	Refer to item 3. general specifications Refer to item 3. general
Temperature Operation	and keep the temperature at -55°C the rated voltage for 45 minutes on Then leaving the tested resistor in 8± 1 hours, and measure its resistance. Refer to MIL-R-55342D 4.7.4	of for 1 hour. Then load not and 15 minutes off to room temperature for ance variance rate.	20/ . 50/ /4 00/ . 0 05 0)	specifications
Whisker Test	○Test item 1 (Thermal Shock the Minimum storage temperature Maximum storage temperature Temperature-retaining time Number of temperature cycles	e -40± 2°C e 85± 2°C 7 min.	Max. 50 μ m	

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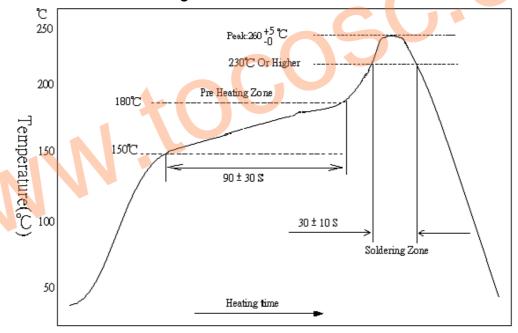
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ITEM	ITEM Conditions -		Specifications	
I I CIVI			Resistors	Jumper
	Test item 2 (Constant t	emperature/humidity test):		
	Temperature	85°C		
	Humidity	85%		
	Testing duration	500± 4 hours		
	☐ Inspection: Inspect for whisker formation on specimens that underwent the acceleration test specified in subciause 4.2, with a magnifier (stere omicroscope) of about 40 or higher magnification. If judgment is hard in this method, use a scanning electron microscope (SEM) of about 1,000 or higher magnification. By SONY (SS-00254-8)			

7 Recommend Soldering Method

7.1 Lead Free Reflow Soldering Profile



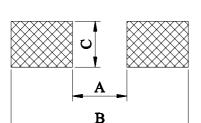
7.2 Soldering Iron: temperature 350 $^{\circ}C^{\pm}$ 10 $^{\circ}C$, dwell time shall be less than 3 sec.

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8 Recommend Land Pattern Design (For Reflow Soldering)



			Office a filling
DIM TYPE	Α	В	С
RTT01	0.3	1.0	0.4
RTT02	0.5	1.5	0.6
RTT03	0.8	2.1	0.9
RTT05	1.2	3.0	1.3
RTT06	2.2	4.2	1.6
RTT12	2.2	4.2	2.8
RTT20	3.5	6.1	2.8
RTT25	3.8	8.0	3.5

9 Marking Diagrams

- 9.1 Resistance Range: \geq 1 Ω
 - 9.1.1 RTT03 \ 05 \ 06 \ 12 \ 20 \ 25 \ \pm 2\% \ \pm 5\% Tolerance:
 - 9.1.1.1 Resistance Range \geq 10 Ω : 3 digits in E-24 series, first two digits are significant figures, third digit is multiplier (10 $^{\times}$).

$$\langle EX \rangle$$
 Marking→100
100=10× 10⁰=10Ω

9.1.1.2 Resistance Range < 10 Ω: 3 digits in E-24 series, first and thrid digits are significant figures, second digit is multiplier (10⁻¹).

$$\langle EX \rangle$$
 Marking→4R7
4R7=47× 10⁻¹=4.7Ω

- 9.1.2 RTT05 \ 06 \ 12 \ 20 \ 25 \ \pm 0.1\% \ \pm 0.5\% \ \pm 1\% Tolerance:
- 9.1.2.1 Resistance Range ≥ 100 Ω: 4 digits in E-24 series or E-96 series, first three digits are significant figures, forth digit is multiplier (10[×]).

$$\langle EX \rangle$$
 Marking→1002
1002=100× 10²=10000Ω=10KΩ

9.1.2.2 Resistance Range < 100 Ω : 4 digits in E-24 series or E-96 series, three digits are significant figures, R digit is multiplier (10 $^{\times}$).

$$\langle\!\langle EX \rangle\!\rangle$$
 Marking \rightarrow 10R2 $\,$,R digit is multiplier (10-1).
$$10R2 = 102 \times \,\, 10^{-1} = 10.2 \, \Omega$$
 Marking \rightarrow 1R02 $\,$,R digit is multiplier (10-2).
$$1R02 = 102 \times \,\, 10^{-2} = 1.02 \, \Omega$$

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9.1.3 RTT03 \pm 0.1% \cdot \pm 0.5% \cdot \pm 1% Tolerance:

For EIAJ-96 Marking.

《EX》Marking→47B

$$47B = 301 \times 10^{1} = 3010 \Omega = 3.01 \text{K}\Omega$$

If the resistance is not in E-96 series and in E-24 series ,the marking is expressed by E-24 series and one short bar under marking letter.

R value > 100Ω :marking 391 391=39 x10¹=390 Ω

R value $< 100 \Omega$:marking 390 390=39 x100=39 Ω

9.2 Resistance Range: <1 Ω

9.2.1 RTT03 \ 05 \ 06 \ 12 \ 20 \ 25 \ \pm 1\% \ \pm 2\% \ \pm 5\% Tolerance:

9.2.1.1 Resistance Range ≥ 100 mΩ: 4 digits in E-24 series or E-96 series, later three digits are significant figures, first digit is multiplier (10⁻³).

《EX》Marking→R220 (E-24 series)

R220=220x $10^{-3} = 0.22 \Omega = 220 \text{m} \Omega$

Marking→R102 (E-96 series)

R102= $102 \times 10^{-3} = 0.102 \Omega = 102 \text{m}\Omega$

9.2.1.2 Resistance Range < 100 mΩ: 4 digits in E-24 series, later two digits are significant figures, first digit is multiplier (10⁻³).

《EX》Marking→R022

 $R022 = 22 \times 10^{-3} = 0.022 \Omega = 22 \text{m} \Omega$

9.3 RTT OR:

9.3.1 RTT03 \times 05 \times 06 \times 12 \times 20 \times 25 \pm 1% Tolrerance : The marking is expressed by " 000 ".

9.3.2 RTT03 × 05 × 06 × 12 × 20 × 25 ± 5% Tolrerance : The marking is expressed by " 0 ".

9.4 RTT01 . RTT02 No Marking

9.5 Marking

9.5.1 E-24 series

10	11	12	13	15	16	18	20	22	24	27	30
33	36	39	43	47	51	56	62	68	75	82	91

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9.5.2 E-96 series

100	102	105	107	110	113	115	118	121	124	127	130
133	137	140	143	147	150	154	158	162	165	169	174
178	182	187	191	196	200	205	210	215	221	226	232
237	243	249	255	261	267	274	280	287	294	301	309
316	324	332	340	348	357	365	374	383	392	402	412
422	432	442	453	464	475	487	499	511	523	536	549
562	576	590	604	619	634	649	665	681	698	715	732
750	768	787	806	825	845	866	887	909	931	953	976

9.5.3 EIAJ-96

This table shows the first two digits for the three-digits EIAJ-96 part marking scheme. The third character is a letter multiplier:

Code	Ω														
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
80	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

 $Y=10^{-2} X=10^{-1} A=10^{0} B=10^{1} C=10^{2} D=10^{3} E=10^{4} F=10^{5}$

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9.6 Marking Standard

Making Type	A	В	C	D	E	F	X	Y			
0603		D			2	H	2	>			
Making Type	1	2	3	4	5	6	7	8	9	0	R
0603	-	2	m		5	5		3	9	C 3	1
0805	1	2	3	1	5	6		8	9	C 3	2
1206	-1	2	3		5	6	7	00	9	CO	R
1210	1	2	3	-1	5	6	7	8	9	CO	R
2010 2512	1	2	3	J	5	6	7	8	9	0	R

10 Plating Thickness $10.1 \text{Ni}: \geq 1 \,\mu\,m$ $10.2 \text{Sn} \text{ (Tin)}: \geq 3 \,\mu\,m$ 10.3 Sn (Tin): Matte Sn

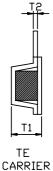
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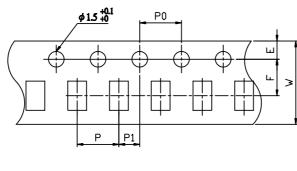
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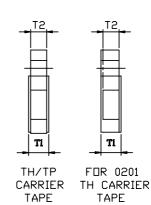
11 Taping Specifications

11.1Tape Dimensions



TAPE



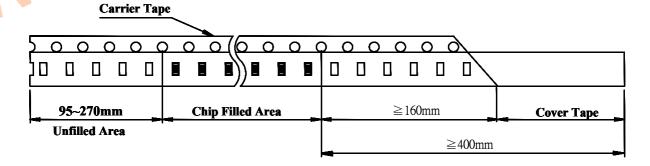


DIRECTION OF FEED

Unit: mm

Packaging	DIM	A	В	w	Е	F	T1	T2	Р	PO	10× P0	P1
TH Carrier	RTT01	0.68± 0.03	0.38± 0.03	8.0± 0.1	1.75± 0.1	3.5± 0.05	0.45+0.1/-0	0.28± 0.02	2.0± 0.05	4.0± 0.05	40.0± 0.20	2.0± 0.05
Tape	RTT02	1.15± 0.05	0.65± 0.05	8.0± 0.2	1.75± 0.1	3.5± 0.05	0.45+0.2/-0	0.45± 0.05	2.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
	RTT03	1.80± 0.1	1.00± 0.1	8.0± 0.2	1.75± 0.1	3.5± 0.05	0.60+0.2/-0	0.60± 0.1	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
TP	RTT05	2.30± 0.1	1.55± 0.1	8.0± 0.2	1.75± 0.1	3.5± 0.05	0.75+0.2/-0	0.75± 0.1	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
Carrier	RTT06	3.50± 0.2	1.90± 0.2	8.0± 0.2	1.75± 0.1	3.5± 0.05	0.75+0.2/-0	0.75± 0.1	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
Tape	RTT12	3.50± 0.2	2.80± 0.2	8.0± 0.2	1.75± 0.1	3.5± 0.05	0.75+0.2/-0	0.75± 0.1	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
	RTT20	5.50± 0.2	2.80± 0.2	12.0± 0.2	1.75± 0.1	5.5± 0.05	0.75+0.2/-0	0.75± 0.1	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
TE	RTT20	5.50± 0.2	2.80± 0.2	12.0± 0.2	1.75± 0.1	5.5± 0.05	0.85± 0.15	0.23± 0.15	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05
Carrier Tape	RTT25	6.70± 0.2	3.40± 0.2	12.0± 0.2	1.75± 0.1	5.5± 0.05	0.85± 0.15	0.23± 0.15	4.0± 0.1	4.0± 0.05	40.0± 0.20	2.0± 0.05

11.2Lead Dimensions:



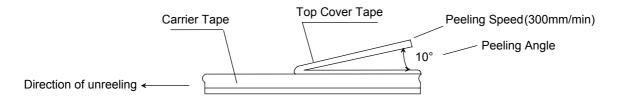
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11.3Cover Tape Peel off Strength

Specifications:0201 \ 0402 => 0.07~0.5 N (7.1~51 gf) 0603 \ 0805 \ 1206 \ 1210 \ 2010 \ 2512 => 0.07~0.7N (7.1~71.4gf)



11.4Packaging QTY:

		<u> </u>													
							Packagi	ing QT\	(pcs /	reel)					
TYPE	Tape	TI			TH		TP			TE					
TIFE	Width			2 mm	Pitch				4 mn	n Pitch			4 mm	Pitch	
		TH	H0	H2	НЗ	H4	H5	TP	P2	P3	P4	TE	E2	E3	E4
RTT01	8 mm	10,000	15,000				50,000				!		-	į	
RTT02	8 mm	10,000		20,000	30,000	40,000	50,000				-				
RTT03	8 mm														
RTT05	8 mm							5,000	10,000	15,000	20,000				
RTT06	8 mm							3,000	10,000	13,000	20,000				
RTT12	8 mm														
RTT20	12 mm											4,000	8,000	12 000	16,000
RTT25	12 mm	-	_ 1					-	-		-	4,000	0,000	12,000	10,000
Reel	Туре	7"	7"	10"	13"	13"	13"	7"	10"	13"	13"	7"	10"	13"	13"

TYPE	Tape Width	Bulk Case (pcs / case)	
RTT02	8 mm		50,000
RTT03	8 mm		25,000
RTT05	8 mm		10,000
RTT06	8 mm	36	5,000
RTT12	8 mm		
RTT20	12 mm	110	
RTT25	12 mm	-	

11.4.1Typical taping type : TH \ TP \ TE

11.4.20ther taping type are upon customer's request.

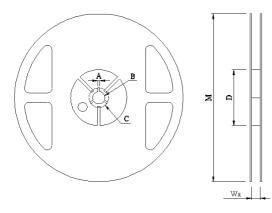
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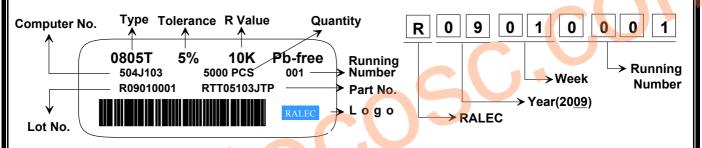
Unit: mm

11.5Reel Dimensions:



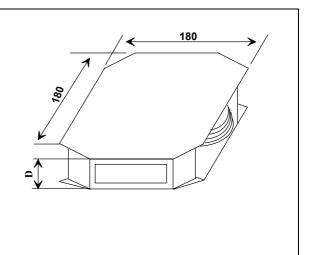
				-		
Reel Type/ Tape	Wa	М	A	В	С	D
7" reel for 8 mm tape	9.0 ± 0.5	178 ± 2.0				60.0 ± 1.0
7" reel for 12 mm tape	13.8 ± 0.5	178 ± 2.0	2.0 ± 0.5	13.5 ± 0.5	21.0 ± 0.5	80.0 ± 1.0
10" reel for 8 mm tape	10.0 ± 0.5	254 ± 2.0	1 0.5			100.0 ± 1.0
13" reel for 8 mm tape	10.0 ± 0.5	330 ± 2.0				100.0 ± 1.0

11.6Label:



11.7 Inner Box

D Dimension (mm)
12
24
36
48
60
72
84
96
108
120



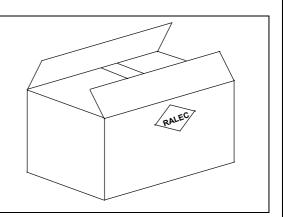
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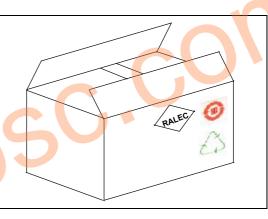
11.8Box

10R Inner Box Number	L(mm)	W(mm)	D(mm)
2	272	205	210
4	375	280	210
8	544	380	210



11.9Box (For China)

10R Inner Box Number	L(mm)	W(mm)	D(mm)
2	272	205	210
4	375	280	210
8	544	380	210



12Measurement Point

Bottom electrode	Unit : mm				
A	TYPE DIM	Α	В		
	RTT01	0.44± 0.05	0.22 ± 0.05		
	RTT02	0.80± 0.05	0.24 ± 0.05		
	RTT03	1.35± 0.05	0.35 ± 0.05		
	RTT05	1.80 ± 0.05	0.35 ± 0.05		
	RTT06	2.90 ± 0.05	0.35 ± 0.05		
• Current Terminal	RTT12	2.90 ± 0.05	0.35 ± 0.05		
Voltage Terminal	RTT20	4.50 ± 0.05	1.15 ± 0.05		
	RTT25	5.90 ± 0.05	1.60 ± 0.05		

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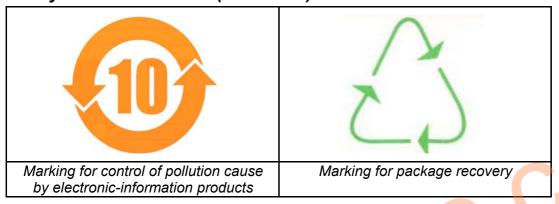
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13 Stock period

13.1The temperature condition must be controlled at $25 \pm 5 \, ^{\circ}C$, the R.H. must be controlled at $60 \pm 15\%$. The stock can maintain quality level in two years.

14The carton packaged for electronic-information products is made by the symbol as follows : (For china)



15For this part. It does not use the materials that include the substances specified in RoHS, the detail refer to the part of prohibition or exchusion items in RoHS(2002/95/EC).

- 15.1 Cadmium and cadmium compounds (permissive content < 100 ppm)
- 15.2 Lead and lead comp<mark>ounds</mark> (pe<mark>rmissive</mark> content < 1000 ppm)
- 15.2.1Exceptions specified:
- 15.2.1.1 Lead contained in the glass of cathode ray tubes, electronic components and fluorescent tubes.
- 15.2.1.2 The glass material used in the electronic components, which includes resistor elements, conductive pastes (silver or copper ones), adhesives, glass frit and sealing materials.
- 15.3 Mercury and its mercury compounds (permissive content < 100 ppm)
- 15.4 Hexavalent chromium compounds (permissive content < 100 ppm)
- 15.5 Polybrominated biphenyls(PBB)(permissive content < 100 ppm)
- 15.6 Polybrominated diphenylethers(PBDE)(permissive content < 100 ppm)

16 Attachments

16.1Document Revise Record Paper

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