



**ARRAY CHIP RESISTORS** 

YC/TC 5%, 1% sizes

YC:102/104/122/124/162/164/248/324/158/358 TC: 122/124/164 RoHS compliant



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IRYN

2R20

621

1221

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**Chip Resistor Surface Mount** YC/TC SERIES 102 to 358

# SCOPE

This specification describes YC (convex, flat) and TC (concave) series chip resistor arrays with leadfree terminations made by thick film process.

# **APPLICATIONS**

- Terminal for SDRAM and DDRAM
- Computer applications: laptop computer, desktop computer
- Consume electronic equipments: PDAs, PNDs
- Mobile phone, telecom...

#### FEATURES

- More efficient in pick & place application
- Low assembly costs
- RoHS compliant
  - · Products with lead free terminations meet RoHS requirements
  - Pb-glass contained in electrodes
  - Resistor element and glass are exempted by RoHS
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Saving of PCB space
- None forbidden-materials used in products/production

#### ORDERING INFORMATION - GLOBAL PART NUMBER & 12NC

Both part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value. YAGEO BRAND ordering code

# **GLOBAL PART NUMBER (PREFERSRED)**

YC,	<u>XXX</u>	_	X	x	X	xx	<b>XXXX</b> (6)	L/T
тс	(I)		(2)	(3)	(4)	(5)	(6)	(7)

# (I) SIZE

YC:102/104/122/124/162/164/248/324/158/358 TC: 122/124/164

#### (2) TOLERANCE

 $F = \pm 1\%$ 

 $| = \pm 5\%$  (for |umper ordering, use code of |)

#### (3) PACKAGING TYPE

R = Paper taping reel

K = Embossed plastic tape reel

# (4) TEMPERATURE COEFFICIENT OF RESISTANCE

- = Base on spec

#### (5) TAPING REEL

07 = 7 inch dia. Reel

13 = 13 inch dia. Reel

#### (6) RESISTANCE VALUE

There are 2~4 digits indicated the resistor value. Letter R/K/M is decimal point.

Detailed resistance rules show in table of "Resistance rule of global part number".

# (7) DEFAULT CODE

Letter L is the system default code for ordering only. <sup>(Note)</sup> Letter T is for YCI02 only

#### Resistance rule of global part number sistanco codo mulo

Resistance code rule	Example
OR	0R = Jumper
XRXX	$IR = I \Omega$
(1 to 9.76 Ω)	$IR5 = I.5 \Omega$
(1 to 7.76 \$2)	9R76 = 9.76 Ω
XXRX	10R = 10 Ω
(10 to 97.6 Ω)	97R6 = 97.6 $\Omega$
XXXR	
(100 to 976 Ω <b>)</b>	$100R = 100 \Omega$
XKXX	IK = 1,000 Ω
(Ι to 9.76 K <b>Ω)</b>	9K76 = 9760 Ω
XM	$IM = I,000,000 \Omega$
(Ι MΩ <b>)</b>	

#### **ORDERING EXAMPLE**

The ordering code of a YCI22 convex chip resistor array, value 1,000  $\Omega$  with ±5% tolerance, supplied in 7-inch tape reel is: YC122-JR-071KL.

#### NOTE

- I. All our RSMD products meet RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART NUMBER / I2NC can be added (both are on customer request)

Chip Resistor Surface Mount YC/TC SERIES 102 to 358

# PHYCOMP BRAND ordering codes

Both GLOBAL PART NUMBER (preferred) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

# **GLOBAL PART NUMBER** (PREFERRED)

For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2. TC122 series is supplied and ordered by global part number only.

#### 12NC CODE

<b>235</b> (I)			(2) (3) (4)			Last dig Resistance	git of 12NG decade <sup>(3)</sup>		Last digit
TYPE/	START	TOL.	RESISTANCE	PAPER / PE TAPE O	N REEL (units) <sup>(2)</sup>	0.01 to 0.0	976 Ω		0
2×0402	IN <sup>(1)</sup>	(%)	RANGE	10,000	50,000	0.1 to 0.97	76 Ω		7
RV321	2350	±5%	l to I MΩ	0 3  xxx	013 12xxx	l to 9.76 (	2		8
RV322	2350	±1%	10 to 1 MΩ	013 2xxxx	013 3xxxx	10 to 97.6	Ω		9
umper	2350	_	0 Ω	013 91001	-	100 to 976	δΩ		
packaging. (3) The remaining 4 or 3 digits represent the resistance val last digit indicating the multiplier as shown in the table					1 to 9.76 N 10 to 97.6			5	
	st digit o	-	• •			Example:	0.02 Ω	=	0200 or 200
4) "L"	is optior	nal sym	nbol <sup>(Note)</sup> .				0.3 Ω	=	3007 or 307
Order	ING EXA	MPLE					ΙΩ	=	1008 or 108
The or	dering co	ode of	a ARV321 resis	stor, value 1,000 $\Omega$ v	vith ±5%		33 KΩ	=	3303 or 333
	ce, supp 22-JR-07		tape of 10,000	units per reel is: 23	500 3   02(L)		10 MΩ	=	1006 or 106

#### ΝΟΤΕ

- I. All our RSMD products are RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead Free Process"
- 2. On customized label, "LFP" or specific symbol printed and the optional "L" at the end of GLOBAL PART

NUMBER / I2NC can be added (both are on customer request)

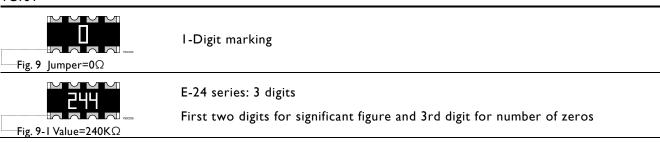
	Chip Resisto	<b>YC/TC</b> SER	IES 102 to 358	
MARKING				
YC102				
Fig. I	jnet42	No marking		
YCI22				_
Fig. 2	J Preción	No marking		
YC104				
Fig. 3	WSC108,1	No marking		
YCI24/164/3	324			
	<b>ο</b> er=0Ω	I-Digit marking	C C · C ·	
		E-24 series: 3 digits		
		First two digits for significant	figure and 3rd digit for number of zeros	
YC248	lue=240KΩ	- FUA		
Fig. 5 Jum	Den en e	I-Digit marking		
		E-24 series: 3 digits		
Fig. 5-I Va	<b>219</b> Iue=240KΩ		figure and 3rd digit for number of zeros	
YC158/358				_
			E-24 series: 3 digits	
	μe=24KΩ		First two digits for significant figure an digit for number of zeros	d

TCI22

No marking Fig. 7 TCI24 Л No marking 7/15/206 Fig. 8

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#### TCI64



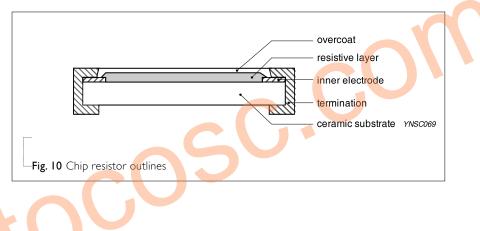
For further marking information, please refer to data sheet "Chip resistors marking".

# **CONSTRUCTION**

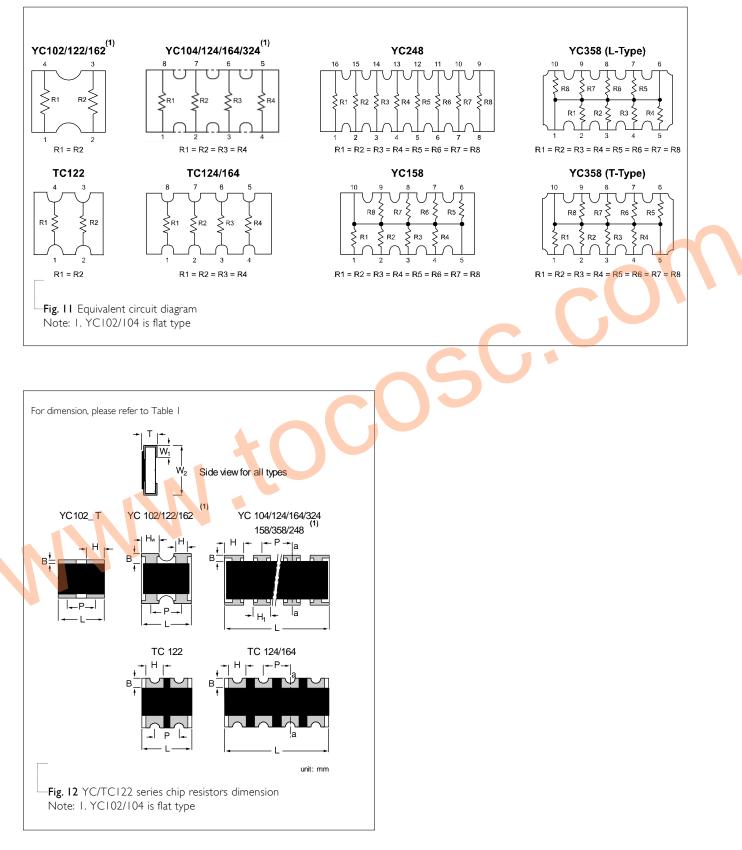
The resistor is constructed on top of a high-grade ceramic body. Internal metal electrodes are added on each end to make the contacts to the thick film resistive element. The composition of the resistive element is a noble metal imbedded into a glass and covered by a second glass to prevent environment influences. The resistor is laser trimmed to the rated resistance value. The resistor is covered with a protective epoxy coat, finally the two external terminations (matte tin on Nibarrier) are added as shown in Fig.9.

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### OUTLINES



SCHEMATIC



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Chip Resistor Surface Mount YC/TC SERIES 102 to 358

#### **DIMENSIONS**

TYPE	$H/H_{I}/H_{W}$	В	Р	L	т	WI	V
YC102_L	H : 0.30 ± 0.10 H <sub>w</sub> : 0.35 ± 0.10	0.15 ±0.10	0.50 ±0.05	0.80 ±0.10	0.35 ±0.10	0.15 ±0.10	0.60 ±0.
YC102_T	H: 0.25 ± 0.10	0.15 ±0.10	0.50 ±0.05	0.80 ±0.10	0.35 ±0.10	0.15 ±0.10	0.60 ±0
YC104	H:0.20 ± 0.10	0.15 ±0.05	0.40 ±0.10	1.40 ±0.10	0.35 ±0.10	0.15 ±0.10	0.60 ±0.
YCI22	H:0.21+0.10/-0.05 H <sub>w</sub> :0.35 ±0.10	0.20 ±0.10	0.67 ±0.05	1.00 ±0.10	0.30 ±0.10	0.25 ±0.10	1.00 ±0.
YCI24	H: 0.45 ± 0.05 H <sub>I</sub> : 0.30 ± 0.05	0.20 ±0.15	0.50 ±0.05	2.00 ±0.10	0.45 ±0.10	0.30 ±0.15	1.00 ±0.
YC162	H:0.30 ±0.10 H <sub>w</sub> :0.65 ±0.15	0.30 ±0.10	0.80 ±0.05	1.60 ±0.10	0.40 ±0.10	0.30 ±0.10	1.60 ±0.
YC164	H : 0.65 <b>±</b> 0.05 H <sub>1</sub> : 0.50 <b>±</b> 0.15	0.30 ±0.15	0.80 ±0.05	3.20 ±0.15	0.60 ±0.10	0.30 ±0.15	1.60 ±0.
YC248	H : 0.45 <b>±</b> 0.05 H <sub>1</sub> : 0.30 <b>±</b> 0.05	0.30 ±0.15	0.50 ±0.05	4.00 ±0.20	0.45 ±0.10	0.40 ±0.15	1.60 ±0.
YC324	H : 1.10 <b>±</b> 0.15 H <sub>1</sub> : 0.90 <b>±</b> 0.15	0,50 <b>±</b> 0,20	1.27 ±0.05	5.08 ±0.20	0.60 ±0.10	0.50 ±0.15	3.20 ±0.
TCI22	H : 0.30 ±0.05	0.25 ±0.15	0.50 ±0.05	1.00 ±0.10	0.30 ±0.10	0.25 ±0.15	1.00 ±0.
TCI24	H:0.30 ±0.10	0.20 ±0.10	0.50 ±0.05	2.00 ±0.10	0.40 ±0.10	0.25 ±0.10	1.00 ±0
TCI64	H:0.50 ±0.15	0.30 ±0.15	0.80 ±0.05	3.20 ±0.15	0.60 ±0.10	0.30 ±0.15	1.60 <b>±</b> 0
YC158	H : 0.45 <b>±</b> 0.05 H <sub>1</sub> : 0.32± 0.05	0.30 ±0.15	0.64 ±0.05	3.20 ±0.20	0.60 ±0.10	0.35 <b>±</b> 0.15	1.60 ±0.
YC358	H : 1.10 <b>±</b> 0.15 H <sub>1</sub> : 0.90 <b>±</b> 0.15	0.50 ±0.15	1.27 ±0.05	6.40 ±0.20	0.60 ±0.10	0.50 <b>±</b> 0.15	3.20 ±0.
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Table	2									
TYPE	POWER P70	OPERATING TEMP. RANGE	MWV	RCOV	DWV		E RANGE & RANCE	T. C. R.	Jumper crit (unit	
YC102	1/32W	-55°C to +125°C	15V	30V	30V	E24/E96 ±1%	$\begin{array}{l}   0\Omega \leq R \leq   M\Omega \\   0\Omega \leq R \leq   M\Omega \\ < 0.05\Omega \end{array}$	1200 180	Rated current Max. current	
YC104	1/32W	-55°C to +125°C	12.5V	25V	25V	E24/E96 ±1%	$\begin{array}{l}   0\Omega \leq R \leq   M\Omega \\   0\Omega \leq R \leq   M\Omega \\ < 0.05\Omega \end{array}$	±200 ppm/°C-	Rated current Max. current	
YC122	1/16W	-55°C to +155°C	50V	100V	100V	E24/E96 ±1%	$ \Omega \le R \le  M\Omega $ $ \Omega \le R \le  M\Omega $ $< 0.05\Omega$		Rated current Max. current	
YCI24	1/16W	-55°C to +155°C	25V	50V	100V	E24/E96 ±1%	$ \Omega \le R \le  M\Omega  \\  \Omega \le R \le  M\Omega  \\ < 0.05\Omega$	$ \Omega \le R \le  0\Omega^{-1}]$ ±250 ppm/°C $ 0\Omega \le R \le  M\Omega]$ ±200 ppm/°C-	Rated current Max. current	
YC162	1/16W	-55°C to +155°C	50V	100V	100V	E/24/E96 ±1%	$ \Omega \le R \le  M\Omega  \\  \Omega \le R \le  M\Omega  \\ < 0.05\Omega$	-200 ppm/ C-	Rated current Max. current	
YC164	1/16W	-55°C to +155°C	50V	100V	100V	E24/E96 ±1%	$ \Omega \le R \le  M\Omega $ $ \Omega \le R \le  M\Omega $ $< 0.05\Omega$		Rated current Max. current	
YC248	1/16W	-55°C to +155°C	50V	100V	100V	E24/E96 ±1%	$ \begin{array}{l}  0\Omega \leq R \leq IM\Omega \\  0\Omega \leq R \leq IM\Omega \\ < 0.05\Omega \end{array} $		Rated current Max. current	
YC324	1/8W	-55°C to +155°C	200V	500V	500V		$\frac{10\Omega \le R \le IM\Omega}{10\Omega \le R \le IM\Omega}$			
TCI22	1/16W	-55°C to +125°C	50V	100V	100V	E24/E96 ±1%	$10\Omega \le R \le 1M\Omega$ $10\Omega \le R \le 1M\Omega$ $< 0.05\Omega$	- ±200 ppm/°C	Rated current Max. current	
TCI24	1/16W	-55°C to +125°C	50V	100V	100V	E24/E96 ±1%	$ \begin{array}{l}   0\Omega \leq R \leq   M\Omega \\   0\Omega \leq R \leq   M\Omega \\ < 0.05\Omega \end{array} $	··· <u>-</u>	Rated current Max. current	
TCI64	1/16W	-55°C to +155°C	50V	1007	100V	E24/E96 ±1%	$ \begin{array}{l}   0\Omega \leq R \leq   M\Omega \\   0\Omega \leq R \leq   M\Omega \\ < 0.05\Omega \end{array} $	-	Rated current Max. current	
YC158	1/16W	-55°C to +155°C	25V	50V	50V	E24 ±5%	$ 0\Omega \le R \le  00K\Omega $	-		
YC358	1/16W	-55°C to +155°C	50V	100V	100V	E24 ±5%	$10\Omega \le R \le$ 330KΩ	-		

# FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please refer to data sheet "Chip resistors mounting".

#### PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and	d packaging quantity									
PACKING STYLE	PACKING STYLE	YC102 /104	YC/TC 122	YC/TC 124	YC162	YC/TC 164	YC248	YC324	YC158	YC358
Paper taping reel ( R )	7" (178mm)	10,000	10,000	10,000	5,000	5,000	5,000		5,000	
	13" (254mm)		50,000	40,000		20,000			20,000	
Embossed taping reel ( K)	7" (178mm)						4,000	4,000		4,000

# ΝΟΤΕ

1. For tape and reel specification/dimensions, please refer to data sheet "Chip resistors packing".

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Chip Resistor Surface Mount YC/TC SERIES 102 to 358

#### FUNCTIONAL DESCRIPTION

# **OPERATING TEMPERATURE RANGE**

YC102/104/122/162, TC122/124 Range:

-55°C to +125°C (Fig.13)

YCI24/164/248/324/158/358, TCI64 Range:

-55°C to +155°C(Fig.14)

# **POWER RATING**

Each type rated power at 70°C YC102/104 = 1/32 W YC122/124/162/164/248/158/358 = 1/16 W YC324 = 1/8 W TC122/124/164 = 1/16 W

# **R**ATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

### $V = \sqrt{(P \times R)}$

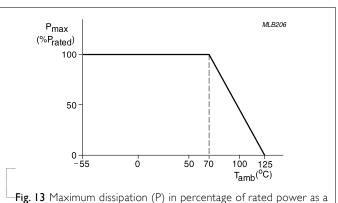
or max. working voltage whichever is less

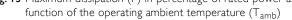
Where

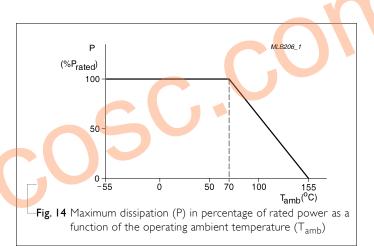
V=Continuous rated DC or AC (rms) working voltage (V)

P=Rated power (W)

R=Resistance value ( $\Omega$ )







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 Chip Resistor Surface Mount
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# TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Life/	MIL-STD-202G-method 108A	1,000 hours at 70±5 °C applied RCWV	±(2%+0.05 Ω)
Operational Life/	IEC 60115-1 4.25.1	1.5 hours on, 0.5 hour off, still air required	<100 m $\Omega$ for Jumper
Endurance	JIS C 5202-7.10		
High Temperature	MIL-STD-202G-method 108A	1,000 hours at maximum operating	±(1%+0.05 Ω)
Exposure/ Endurance at	IEC 60115-1 4.25.3 JIS C 5202-7.11	temperature depending on specification, unpowered	$<\!50~{ m m}\Omega$ for Jumper
Upper Category Temperature	JIG C 3202 7.11	No direct impingement of forced air to the parts	
		Tolerances: 125±3 °C	
Moisture	MIL-STD-202G-method 106F	Each temperature / humidity cycle is defined at	±(2%+0.05 Ω)
Resistance	IEC 60115-1 4.24.2	8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	< 100 m $\Omega$ for Jumper
		Parts mounted on test-boards, without condensation on parts	
		Measurement at 24±2 hours after test conclusion	
Thermal Shock	MIL-STD-202G-method 107G	-55/+125 ℃	±(1%+0.05 Ω)
		Note: Number of cycles required is 300. Devices mounted	$<$ 50 m $\Omega$ for Jumper
	N.	Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
Short Time	MIL-R-55342D-para 4.7.5	2.5 times RCWV or maximum overload	±(2%+0.05 Ω)
Overload	IEC60115-14.13	voltage whichever is less for 5 sec at room	${<}50~{ m m}\Omega$ for Jumper
		temperature	No visible damage
Board Flex/	IEC60115-14.33	Device mounted on PCB test board as	±(1%+0.05 Ω)
Bending		described, only I board bending required	<50 m $\Omega$ for Jumper
		3 mm bending	No visible damage
		Bending time: 60±5 seconds	

Chip Resistor Surface Mount YC/TC SERIES 102 to 358

- · · · ·	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability			
- Wetting	IPC/JEDECJ-STD-002B test B	Electrical Test not required	Well tinned (≥95% covered No visible damage
	IEC 60068-2-58	Magnification 50X	TNO VISIDIE Gairlage
		SMD conditions:	
		I <sup>st</sup> step: method B, aging 4 hours at 155 °C dry heat	
		$2^{nd}$ step: leadfree solder bath at 245±3 °C	
		Dipping time: 3±0.5 seconds	
- Leaching	IPC/JEDECJ-STD-002B test D	Leadfree solder, 260 °C, 30 seconds	No visible damage
	IEC 60068-2-58	immersion time	
- Resistance to	MIL-STD-202G-method 210F	Condition B, no pre-heat of samples	±(1%+0.05 Ω)
Soldering Heat	IEC 60068-2-58	Leadfree solder, 270 °C, 10 seconds	<50 m $\Omega$ for Jumper
		immersion time	No visible damage
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	$\mathcal{C}\mathcal{O}$
		COSU	

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### **REVISION HISTORY**

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 3	Apr. 29, 2016	-	- Update YC series and TC164 dimension
Version 2	Dec. 11, 2015	-	- Update Operating Temperature
Version I	Feb. 04, 2015	-	- Update YCI02 to flat type
Version 0	Nov. 14, 2014	-	- First issue of this specification

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