



Specification for Approval

Date: 2012/07/13

Customer: 協裕(威達電

TAI-TECH P/N:	HPC3015F-SERIES	
CUSTOMER P/N:		
DESCRIPTION:		
QUANTITY:	pcs	

REMARK:			
Cu	stomer Approval Feedba	ack	
4 (5	

西北臺慶科技股份有限公司 TAI-TECH Advanced Electronics Co., Ltd

■ 西北臺慶科技股份有限公司

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R&D Center

APPROVED	CHECKED	DRAWN
楊祥忠	詹偉特	蕭羽恬
Mikey Yang	Jack Chan	Yu-tian Siao

Power Inductor

HPC3015F-SERIES

	ECN HISTORY LIST							
REV D	ATE	DESCRIPTION	APPROVED	CHECKED	DRAWN			
1.0	12/07/13	新 發 行	楊祥忠	詹偉特	蕭羽恬			
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Power Inductor

HPC3015F-SERIES

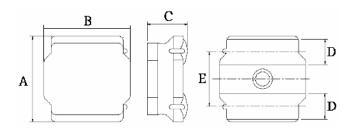
1. Features

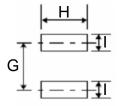
- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

(Halogen) Halogen-free



2. Dimension





Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	G(mm)	H(mm)	l(mm)
HPC3015F	3.0±0.1	3.0±0.1	1.5 max.	0.9±0.2	1.9±0.2	2.2 ref.	2.7 ref.	0.8 ref.

Units: mm

3. Part Numbering

HPC 3015 F - 2R2 M
A B C D E

A: Series

B: Dimension

C: Control S/N

D: Inductance 2R2=2.2uH

E: Inductance Tolerance M=±20%; Y=±30%

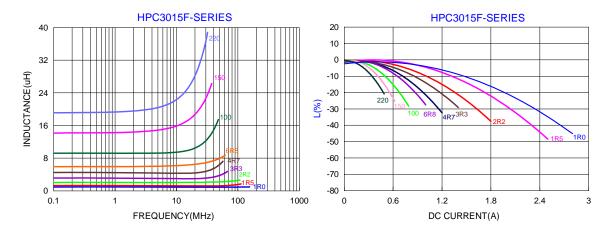
4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	SRF (MHz) min.	DCR (Ω) ±20%	l sat (A)	I rms (A)
HPC3015F-1R0Y	1.0	±30%	1V100K	100	0.030	2.10	2.10
HPC3015F-1R5Y	1.5	±30%	1V100K	87	0.040	1.80	1.82
HPC3015F-2R2M	2.2	±20%	1V100K	64	0.060	1.48	1.50
HPC3015F-3R3M	3.3	±20%	1V100K	49	0.080	1.21	1.23
HPC3015F-4R7M	4.7	±20%	1V100K	40	0.120	1.02	1.04
HPC3015F-6R8M	6.8	±20%	1V100K	36	0.160	0.87	0.88
HPC3015F-100M	10	±20%	1V100K	28	0.230	0.70	0.71
HPC3015F-150M	15	±20%	1V100K	23	0.360	0.56	0.56
HPC3015F-220M	22	±20%	1V100K	20	0.520	0.47	0.47

Note:

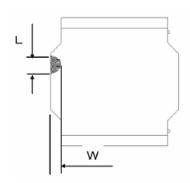
 $\mbox{Isat}: \mbox{Based on inductance change} \quad (\, \triangle \mbox{L/L0}: \leqq \mbox{-30\%} \,) \, \, @ \, \mbox{ambient temp.} \, 25 ^{\circ}\! \mathbb{C}$

Irms: Based on temperature rise $(\triangle T : 40^{\circ}C \text{ typ.})$



Core chipping

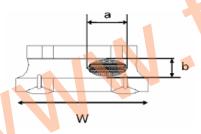
The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



Туре	L	W
HPC3015F	0.6mm Max.	0.6mm Max.

Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.



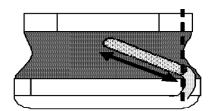
Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

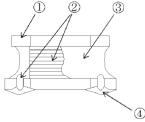
- 1. Width direction (dimension a): Acceptable when $a \le w/2$ Nonconforming when a > w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- 3. When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

External appearance criterion for exposed wire

Exposed end of the winding wire at the secondary side should be 2mm and below.



5. Material List



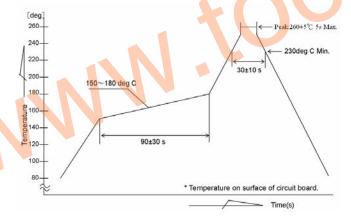
No.	Item	Material
1	Core	Ni-Zn ferrite
2	Wire	Copper Wire
3	Coating	Ероху
4	Solder	Lead free

6. Reliability and Test Condition

Item	Performance	Test Method and Remarks
Operating Temperature	- 25 ~ +120℃.	Including self-generated heat
Storage Temperature	-40 \sim +85 $^{\circ}{\!$	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–25°C to +85°C. With reference to inductance value at+20 °C, change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20 °C, change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arroundindicating until deflection of the test board reaches to 2mm. Proceedings of the sample of the test board reaches to 2mm. Results of the test board reaches to 2mm.
	*OCO	Substrate size : 100x40x1.0 Substrate material : glass epoxy-resin Solder cream thickness : 0.10 2.7
Adhesion o <mark>f Terminal electrode</mark>	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s Solder cream thickness: 0.15
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new solde	The test samples shall be dipped in flux, and then immerse in molten solder as shown in below. Flux: methanol solution containing rosin 25% solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.
Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	The test sample shall be exposed to reflow oven at 230±5% for 40 seconds, with peak temperature at 260±5% for 5 seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin

IAI-TECH		1.0		
Item	Performance	Test Method and Remarks		
Thermal shock		The test samples shall be soldered to the test board by the reflow. The test samples shall be placed at specified temperature for specified time by step 1 to step 4 as shown below in sequence. The temperature cycles shall be repeated 100 cycles . Phase Temperature(*C) Time(min.) 1 -40±3**C 30±3 2 Room Temp Within 3 3 85±2**C 30±3 4 Room Temp Within 3		
Damp heat life test		Test Method and Remarks The test samples shall be soldered to the test board by the reflow. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below. Temperature: 60±2°C Humidity: 90–95%RH Time: 500+24/-0 hrs		
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below. Temperature: 60±2°C Humidity: 90–95%RH Applied current: Rated current Time: 500+24/-0 hrs		
Low temperature life test		The test samples shall be soldered to the test board by the reflow. After that, the test samples shall be placed at test conditions as shown in below. Temperature:-40±2°C Time:500+24/-0 hrs		
Loading at high temperature life test		The test samples shall be soldered to the test board by the reflow. Temperature: 85±2°C. Applied current: Rated current Time: 500+24/-0 hrs.		

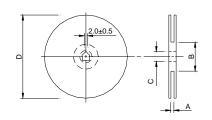
7. Soldering

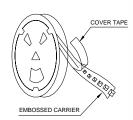


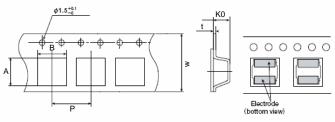
8. Packaging Information

(1) Reel Dimension

(2) Tape Dimension







Туре	A(mm)	B(mm)	C(mm)	D(mm)
HPC3015F	10±1.5	60±1.0	13±0.5	180±0.5

Туре	A(mm)	B(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HPC3015F	3.2±0.1	3.2±0.1	1.9±0.1	4.0±0.1	8.0±0.2	0.30±0.05

(3) Packaging Quantity

Туре	Chip / Reel		
HPC3015F	2000		

Application Notice

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.



For Question Please Contact with SGS www.tw.sgs.com

測試報告 Test Report

號碼(No.): CE/2012/24506 日期(Date): 2012/02/29 頁数(Page): 1 of 7

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(東莞臺廣精密電子有限公司 / TAI-TECH ADVANCED ELECTRONICS (DONGGUAN) CO., LTD.)

(臺慶精密電子(昆山)有限公司 / TAI-TECH ADVANCED ELECTRONICS (KUN-SHAN) CO., LTD.)

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(廣東省東莞市黄江鎮黄牛埔福祥街2號 / NO. 2, FUXIANG STREET, HUANGNIUPU, HUANGJIANG TOWN, DONGGUAN, GUANGDONG) (江蘇省昆山市筵朗昆嘉高科技工業區郭澤路 / GUO-ZE ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA)

以下測試樣品係由客户送樣, 且由客户聲稱並經客户確認如下 (The following samples was/were submitted and identified by/on behalf of the client as):

樣品名稱(Sample Description)

TLP, HPC, TLPC, SPC, TLPH, SPI SERIES

樣品型號(Style/Item No.)

TLP, HPC, TLPC, SPC, TLPH, SPI SERIES

收件日期(Sample Receiving Date)

2012/02/21

測試期間(Testing Period)

2012/02/21 TO 2012/02/29

測試結果(Test Results)

(Please refer to next pages).

Chenyu Kung / Signed for and of the half SGS TAIWAN LTD. Chemical Laboratory - Taipei



测試報告 Test Report

號碼(No.): CE/2012/24506 日期(Date): 2012/02/29 頁數(Page): 2 of 7

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測試結果(Test Results)

測試部位(PART NAME) No.1 :

整體混測 (6款) (MIXED ALL PARTS (6 KINDS))

測試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法偵測 極限値 (MDL)	無 (Result) No.1
鎘 / Cadmium (Cd)	mg/kg	參考IEC 62321: 2008方法, 以感 應耦合電漿原子發射光譜儀檢測。 / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
部 / Lead (Pb)	mg/kg	參考IEC 62321: 2008方法, 以感 應耦合電漿原子發射光譜儀檢測. / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
汞 / Mercury (Hg)	mg/kg	參考IEC 62321: 2008方法, 以感 應耦合電漿原子發射光譜儀檢測. / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
六價鉻 / Hexavalent Chromium Cr(VI)	mg/kg	參考1EC 62321: 2008方法,以UV-VIS檢測. / With reference to IEC 62321: 2008 and performed by UV-VIS.	2	n.d.
鹵素 / Halogen				
鹵素(氟)/ Halogen-Fluorine (F) (CAS No.: 14762-94-8)			50	n.d.
鹵素(氣)/ Halogen-Chiorine (C1) (CAS No.: 22537-15-1)	mg/kg	参考BS EN 14582:2007, 以離子層 析儀分析./ With reference to	50	n.d.
鹵素(溴)/ Halogen-Bromine (Br) (CAS No.: 10097-32-2)		BS EN 14582:2007. Analysis was performed by IC.	50	n.d.
鹵素(碘)/ Halogen-Iodine (I) (CAS No.: 14362-44-8)			50	n.d.

子副檢驗料按股份有關公司

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Manalana Saka CCC Carrie



測試報告

Test Report

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测試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法偵測 極限値 (MDL)	結果 (Result) No.1
多溴聯苯總和 / Sum of PBBs		.s.	_	n.d.
一溴聯苯 / Monobromobiphenyl			5	n.d.
二溴聯苯 / Dibromobiphenyl			5	n.d.
三溴聯苯 / Tribromobiphenyl			5	n.d.
四溴聯苯 / Tetrabromobipheny!			5	n.d.
五溴聯苯 / Pentabromobiphenyl			5	n.d.
六溴聯苯 / Hexabromobiphenyl			5	n.d.
七溴聯苯 / Heptabromobiphenyl			5	n.d.
八溴聯苯 / Octabromobiphenyl			5	n.d.
九溴聯苯 / Nonabromobiphenyl		参考IEC 62321: 2008方法,以氣	55	n.d.
十溴聯苯 / Decabromobiphenyl		相層析儀/質譜儀檢測./With	5	n.d.
多溴聯苯酰總和 / Sum of PBDEs	mg/kg	reference to IEC 62321: 2008	<u> </u>	n.d.
一溴聯苯醚 / Monobromodiphenyl ether		and performed by GC/MS.	5	n.d.
二溴聯苯醚 / Dibromodiphenyl ether			5	n.d.
三溴聯苯醚 / Tribromodiphenyl ether			5	n.d.
四溴聯苯醚 / Tetrabromodiphenyl ether			5	n.d.
五溴聯苯醚 / Pentabromodiphenyl ether			5	n.d.
六溴聯苯醚 / Hexabromodiphenyl ether			5	n.d.
七溴聯苯醚 / Heptabromodiphenyl ether			5	n.d.
八溴聯苯醚 / Octabromodiphenyl ether			5	n.d.
九溴聯苯醚 / Nonabromodiphenyl ether			5	n.d.
十溴聯苯醚 / Decabromodiphenyl ether			5	n.d.

儲註(Note):

- | pril mg/kg = ppm; 0.1wt% = 1000ppm
 - 2. n.d. = Not Detected (未檢出)
 - 3. MDL = Method Detection Limit (方法偵測極限値)
 - 4. "-" = Not Regulated (無規格值)
 - 5. 樣品的測試是基於申請人要求混合測試,報告中的混合測試結果不代表其中個别單一材質的含量. (The samples was/were analyzed on behalf of the applicant as mixing sample in one testing. The above results was/were only given as the informality value.)

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测试报告 Test Report

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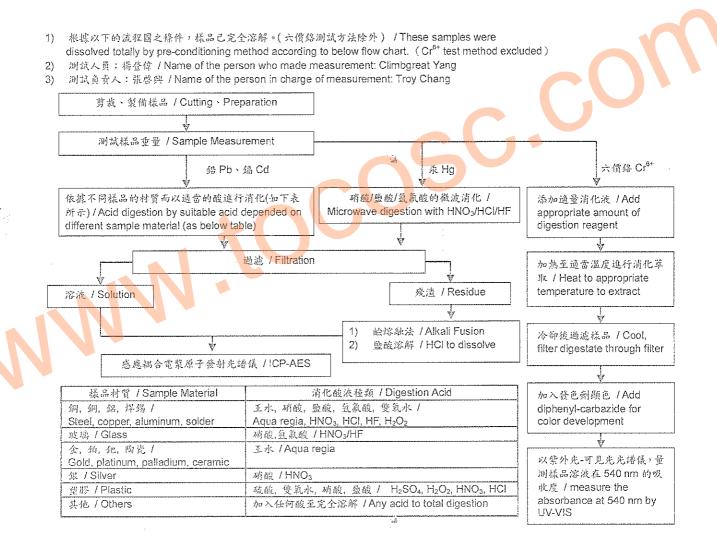
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台灣檢驗科技股份有關公司



测试报告

Test Report

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西北臺慶科技股份有限公司 / TAI-TECH ADVANCED ELECTRONICS CO., LTD. (東莞臺慶精密電子有限公司 / TAI-TECH ADVANCED ELECTRONICS (DONGGUAN) CO., LTD.) (臺慶精密電子(昆山)有限公司 / TAI-TECH ADVANCED ELECTRONICS (KUN-SHAN) CO., LTD.) 桃園縣楊梅市幼獅工業區幼四路1之1號 / NO. 1, YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI CITY,

TAO-YUAN HSIEN, TAIWAN, R. O. C.) (质束省束莞市黄江鎮黄牛埔福祥街2號 / NO. 2, FUXIANG STREET, HUANGNIUPU, HUANGJIANG TOWN, DONGGUAN, GUANGDONG) (江蘇省昆山市筵朗昆嘉高科技工業區郭澤路 / GUO-ZE ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA)

多溴聯苯/多溴聯苯醚分析流程圖 / PBB/PBDE analytical FLOW CHART

測試人員: 翁賜彬 / Name of the person who made measurement: Roman Wong

測試負責人:張啓興 / Name of the person in charge of measurement: Troy Chang

初次测试程序 / First testing process ---

確認程序 / Confirmation process --- □ □

Sample / 樣品 Sample pretreatment / 樣品前處理 Screen analysis / 物篩分析 Sample extraction 樣品萃取/ Soxhlet method 索式萃取法 Concentrate/Dilute Extracted solution 萃取液濃縮/稀釋 Filter / 萃取液過濾 Analysis by GC/MS / 氣相層析質譜儀分析 Issue Report 撰打報告

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測試報告 Test Report

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鹵素分析流程圖 / Analytical flow chart of halogen content

測試人員: 陳恩臻 / Name of the person who made measurement: Rita Chen

測試負責人:張啓興 / Name of the person in charge of measurement: Troy Chang

樣品前處理/分樣 / Sample pretreatment/separation 秤重及將樣品放入樣品槽中/ Weighting and putting sample in cell 燃燒彈/吸收 Oxygen Bomb Combustion / Absorption 稀釋至固定體積 / Dilution to fixed volume 離子層析儀分析 / Analysis was performed by IC

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测量乘台

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* 照片中如有箭頭標示,則表示為實際檢測之樣品/部位. *

(The tested sample / part is marked by an arrow if it's shown on the photo.)



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