



Specification for Approval

Date: 2012/2/16

Customer: 協裕

TAI-TECH P/N: CPI201610HF-Series

CUSTOMER P/N:

DESCRIPTION:

QUANTITY: pcs

REMARK:			
Cu	stomer Approval Feedba	nck	
*		7	

西北臺慶科技股份有限公司 TAI-TECH Advanced Electronics Co., Ltd

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High Current Ferrite Chip Inductor (Lead Free)

CPI201610HF -Series

1.Features

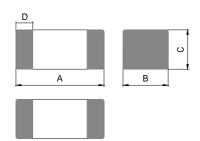
- 1. 2.0x1.6 mm and 1 mm in height (very compact size): CAE and fine printing technology made this compact size possible
- 2. Stable minimum DC resistance in the class.
- 3. High speed mounting: Using SMT mounter makes less than a second mounting possible.
- 4. Excellent mounting strength by SMD chip making.
- 5. Reduced noise over 2/3 of coil inductor by optimal design of CAD Completely lead-free product and support lead-free solder.





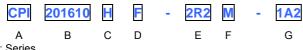


2. Dimensions



Chip Size				
Series A(mm) B(mm) C(mm) D(mm)				
201610	2.0±0.3	1.6±0.2	1.0 max	0.5±0.3

3. Part Numbering



A: Series

B: Dimension

C: Category Code

D: Material

E: Inductance F: Inductance Tolerance

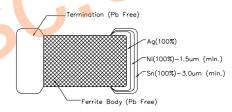
G: Rated Current

LxW

Lead Free Material

2R2=2.2uH M=±20%

1A2=1200mA



4. Specification

Tai-Tech	Inductance(uH)	Test Frequency	Rated Current	DCR	SRF
Part Number	muuctance(un)	(MHz)	(mA) max.	((MHz) min.
CPI201610HF-1R0M-1A3	1.0±20%	1	1300	0.12±30%	110
CPI201610HF-1R5M-1A3	1.5±20%	1	1300	0.12±30%	75
CPI201610HF-2R2M-1A2	2.2±20%	1	1200	0.14±30%	70
CPI201610HF-3R3M-1A0	3.3±20%	1	1000	0.18±30%	50
CPI201610HF-4R7M-0A9	4.7±20%	1	900	0.23±30%	40

5. Reliability and Test Condition

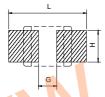
Item	Performa	ince	Test Condition
Operating Temperature	-40~+105 (Including s		
Transportation Storage Temperature	-40~+10	For long storage conditions, please see the Application Notice	
Inductance (Ls)	Refer to standard electrical characteristics	list	Agilent4291 Agilent E4991 Agilent4287 Agilent16192
DC Resistance			Agilent 4338 DC Power Supply
Rated Current			Over Rated Current requirements, there will be some risk
Temperature Rise Test	Rated Current < 1A ΔT 20 Max Rated Current 1A ΔT 40 Max		Applied the allowed DC current. Temperature measured by digital surface thermometer.
	Appearance: No significant abnormality.	No mechanical damage.	Preheat: 150 ,60sec. Solder: Sn-Cu0.5 Solder temperature: 260±5 Flux for lead free: ROL0 Dip time: 10±0.5sec.
Solder heat Resistance	Inductance change: Within ± 10%.	Remaining terminal electrode:75% min.	Preheating Dipping Natural cooling 260°C 150°C 60 10±0.5 second
Solderability	electrode should be covered	Preheating Dipping Natural cooling 45°C 60 60 4±1 second	Preheat: 150 ,60sec. Solder: Sn-Cu0.5 Solder temperature: 245±5 Flux for lead free: ROL0 Dip time: 4±1sec.
Terminal strength	The terminal electrode and the dielectric mot be damaged by the forces applied on the right conditions.	→VV	Size Force (Kgf) Time(sec) 2016 0.6 >30
Flexture strength	The terminal electrode and the dielectric mot be damaged by the forces applied on tright conditions.	/i\	Solder a chip on a test substrate, bend the substrate by 2mm (0.079in)and return. The duration of the applied forces shall be 60 (+ 5) Sec.
Bending Strength	The ferrite should not be damaged by Forces applied on the right condition.	R 0.5(0.02)	Size mm(inches) P-Kgf 2016 1.40(0.055) 1.0
Random Vibration Test	Appearance: Cracking, chipping and any of characteristics should not be allowed. Inductance: within±10%	other defects harmful to the	Frequency: 10-55-10Hz for 15 min. Amplitude: 1.52mm Directions and times: X, Y, Z directions for 15 min This cycle shall be performed 12 times in each of three mutually perpendicular directions (Total 9hours).
Drop	a: No mechanical damage b: Inductance change: : within±10%		Drop 10 times on a concrete floor from a height of 75cm

Item	Perform	Test Condition	
Life testing at High Temperature	Appearance: no damage.		Temperature: 105±2 Applied current rated current. Duration: 1008±12hrs. Measured at room temperature after placing for 2 to 3hrs.
Humidity	Inductance: within±10%of initial value.		Humidity: 90~95%RH. Temperature: 40±2 . Duration: 504±8hrs. Measured at room temperature after placing for 2 to 3hrs.
Thermal shock	Appearance: no damage Inductance: within±10%of initial value.	Phase Temperature() Time(min.) 1 -40±2 30±5 2 room temp. 0.5 3 +105±2 30±5 Measured: 500 times	Condition for 1 cycle Step1: -40±2 30±5 min. Step2: +105±2 30±5 min. Number of cycles: 500 Measured at room temperature after placing for 2 to 3 hrs.
Low temperature storage test			Temperature: -40±2 . Duration: 500±8hrs. Measured at room temperature after placing for 2 to 3hrs.

6. Soldering and Mounting

6-1. Recommended PC Board Pattern

Chip Size						Pattern ow Sold		
Serie	Туре	A(mm)	B(mm)	C(mm)	D(mm)	L(mm)	G(mm)	H(mm)
CPI	201610	2.0±0.3	1.6±0.20	1.0 max	0.5±0.30	3.00	1.00	1.60



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

Products shall be positioned in the sideway direction against the mechanical stress to prevent failure.

6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools. Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1.

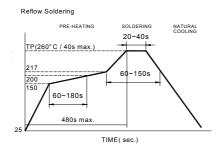
6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

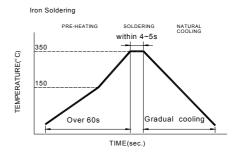
Preheat circuit and products to 150 350 tip temperature (max)

Never contact the ceramic with the iron tip 1.0mm tip diameter (max)

Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1



Iron Soldering times: 1 times max Fig.2

6-2.3 Solder Volume:

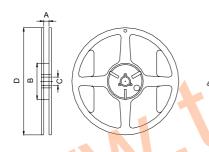
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



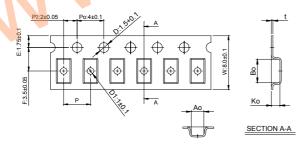
7. Packaging Information

7-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9±0.5	60±2	13.5±0.5	178±2

7-2 Tape Dimension / 8mm



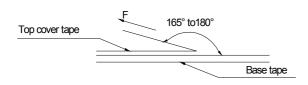
Size	P(mm)	Bo(mm)	Ao(mm)	Ko(mm)	t(mm)
201610	4.0±0.1	2.40±0.20	1.95±0.15	1.45max.	0.30max.

TAI-TECH P5.

7-3. Packaging Quantity

Chip size	201610
Reel	3000
Inner box	15000
Middle box	75000
Carton	150000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Temp. Room Humidity		Tearing Speed
()	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

Storage Conditions

To maintain the solder ability of terminal electrodes:

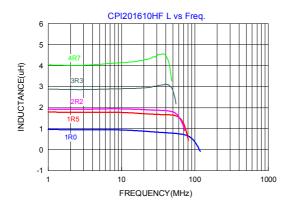
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: -10~ 40 and 30~70% RH.
- 3. Recommended products should be used within 6 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

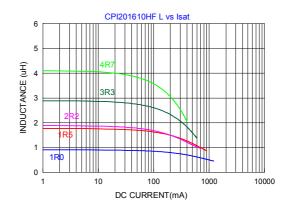
Transportation

MW.

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

Typical Inductance v.s. Frequency Curve







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以下測試樣品係由客户送樣, 且由客户聲稱並經客户確認如下 (The following samples was/were submitted and identified by/on behalf of the client as):

樣品名稱(Sample Description)

: FERRITE CHIP BEAD INDUCTOR ARRAY MCF MCM YMV SERIES

樣品型號(Style/Item No.)

: FERRITE CHIP BEAD INDUCTOR ARRAY MCF MCM YMV SERIES

收件日期(Sample Receiving Date)

: 2011/12/09

測試期間(Testing Period)

: 2011/12/09 TO 2011/12/16

測試結果(Test Results) : 請見下一頁 (Please refer to next pages).

Chenyu Kung Aperation Manager Signed for and on behalf NA SGS TAIWAN LTD. Chemical Laboratory – Taipei

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號碼(No.): CE/2011/C1821 日期(Date): 2011/12/16 頁數(Page): 2 of 12

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測試結果(Test Results)

測試部位(PART NAME)No.1

: 整體混測(10款)(MIXED ALL PARTS (10 KINDS))

測試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法偵測 極限値 (MDL)	結果 (Result) No.1
鍋 / Cadmium (Cd)	mg/kg	參考IEC 62321: 2008方法, 以感應耦合電 漿原子發射光譜儀檢測. / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
鉛 / Lead (Pb)	mg/kg	參考IEC 62321: 2008方法, 以感應耦合電 漿原子發射光譜儀檢測. / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
汞 / Mercury (Hg)	mg/kg	參考IEC 62321: 2008方法, 以感應耦合電 漿原子發射光譜儀檢測. / With reference to IEC 62321: 2008 and performed by ICP-AES.	2	n.d.
六價絡 / Hex <mark>av</mark> alent Chromium Cr(VI)	mg/kg	多考IEC 62321: 2008方法,以UV-VIS檢測. / With reference to IEC 62321: 2008 and performed by UV-VIS.	2	n.d.
全氟辛烷磺酸 / Perfluorooctane sulfonates (PFOS-Acid, Metal Salt, Amide)	mg/kg	参考US EPA 3540C: 1996方法,以液相層析/質譜儀檢測全氣辛烷磺酸含量. / With reference to US EPA 3540C: 1996 method for PFOS Content. Analysis was performed by LC/MS.	10	n.d.
全氟辛酸(銨)/ PFOA (CAS No.: 335-67-1)	mg/kg	參考US EPA 3540C: 1996方法,以液相層析/質譜儀檢測全氟辛酸(銨)含量./ With reference to US EPA 3540C: 1996 method for PFOA Content. Analysis was performed by LC/MS.	10	n.d.

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測試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法偵測 極限値 (MDL)	結果 (Result) No.1
六溴環十二烷 /	mg/kg	參考US EPA 3540C方法,以氣相層析/質譜	5	n.d.
Hexabromocyclododecane (HBCDD)		【儀檢測. / With reference to US EPA		
(CAS No.: 25637-99-4)		3540C method. Analysis was performed		
		by GC/MS.		
鄰苯二甲酸甲苯基丁酯 / BBP	%	參考EN 14372, 以氣相層析/質譜儀檢測之.	0.003	n.d.
(Benzyl butyl phthalate) (CAS		/ With reference to EN 14372. Analysis		
No.: 85-68-7)	_	was performed by GC/MS.		
鄰苯二甲酸二 (2-乙基己基)酯 /	%	麥考EN 14372, 以氣相層析/質譜儀檢測之.	0.003	n.d.
DEHP (Di- (2-ethylhexyl)		/ With reference to EN 14372. Analysis		
phthalate) (CAS No.: 117-81-7)		was performed by GC/MS.		
鄰苯二甲酸二異癸酯 / DIDP (Di-	%	參考EN 14372, 以氣相層析/質譜儀檢測之.	0.01	n.d.
isodecyl phthalate) (CAS No.:		/ With reference to EN 14372. Analysis		
26761-40-0)		was performed by GC/MS.		
鄰苯二甲酸二異壬酯 / DINP (Di-	%	參考EN 14372, 以氣相層析/質譜儀檢測之.	0.01	n.d.
isononyl phthalate) (CAS No.:		/ With reference to EN 14372. Analysis		
28553-12-0)		was performed by GC/MS.		
鄰苯二甲酸二正辛酯 / DNOP (Di-n-	%	參考EN 14372, 以氣相層析/質譜儀檢測之.	0.003	n.d.
octyl phthalate) (CAS No.: 117-		/ With reference to EN 14372. Analysis		
84-0)		was performed by GC/MS.		
鄰苯二甲酸二丁酯 / DBP (Dibutyl	%	參考EN 14372, 以氣相層析/質譜儀檢測之.	0.003	n.d.
phthalate) (CAS No.: 84-74-2)		/ With reference to EN 14372. Analysis		
		was performed by GC/MS.		

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西北臺慶科技股份有限公司 / TAI-TECH ADVANCED ELECTRONICS CO., LTD.

70000 A

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測試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法偵測 極限値 (MDL)	結果 (Result) No.1
多溴聯苯總和 / Sum of PBBs		参考IEC 62321: 2008方法, 以氣相層析儀/質譜儀檢測. / With reference to IEC 62321: 2008 and performed by GC/MS.	<u> </u>	n.d.
一溴聯苯 / Monobromobiphenyl			5	n.d.
二溴聯苯 / Dibromobiphenyl			5	n.d.
三溴聯苯 / Tribromobiphenyl			5	n.d.
四溴聯苯 / Tetrabromobiphenyl	1		5	n.d.
五溴聯苯 / Pentabromobiphenyl			5	n.d.
六溴聯苯 / Hexabromobiphenyl			5	n.d.
七溴聯苯 / Heptabromobiphenyl			5	n.d.
八溴聯苯 / Octabromobiphenyl			5	n.d.
九溴聯苯 / Nonabromobiphenyl			5	n.d.
十溴聯苯 / De <mark>cabromobiphenyl</mark>	4.		5	n.d.
多溴聯苯醚總和 / Sum of PBDEs	mg/kg		<u>.</u>	n.d.
一溴聯苯醚 / Monobromodiphenyl ether			5	n.d.
二溴聯苯醚 / Dibromodiphenyl ether			5	n.d.
三溴聯苯醚 / Tribromodiphenyl ether			5	n.d.
四溴哪苯醚 / Tetrabromodiphenyl ether	r		5	n.d.
五溴聯苯醚 / Pentabromodiphenyl ether			5	n.d.
六溴聯苯醚 / Hexabromodiphenyl ether			5	n.d.
七溴聯苯醚 / Heptabromodiphenyl ether			5	n.d.
八溴聯苯醚 / Octabromodiphenyl ether			5	n.d.
九溴聯苯醚 / Nonabromodiphenyl ether			5	·n.d.
十溴哪苯醚 / Decabromodiphenyl ether			5	n.d.

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測試項目 (Test Items)	單位 (Unit)	測試方法 (Method)	方法侦測 極限値 (MDL)	結果 (Result) No.1
鹵素 / Halogen				
鹵素(氟)/ Halogen-Fluorine (F) (CAS No.: 14762-94-8)	mg/kg	参考BS EN 14582:2007, 以離子層析儀分析. / With reference to BS EN 14582:2007. Analysis was performed by IC.	50	n.d.
鹵素(氣)/ Halogen-Chlorine (C1) (CAS No.: 22537-15-1)			50	n.d.
鹵素(溴)/ Halogen-Bromine (Br) (CAS No.: 10097-32-2)			50	n.d.
鹵素 (碘) / Halogen-Iodine (I) (CAS No.: 14362-44-8)			50	n.d.

備註(Note):

- 1. mg/kg = ppm; 0.1wt% = 1000ppm
- 2. n.d. = Not Detected (未檢出)
- 3. MDL = Method Detection Limit (方法偵測極限値)
- 4. "-" = Not Regulated (無規格值)
- 5. 樣品的測試是基於申請人要求混合測試,報告中的混合測試結果不代表其中個别單一材質的含量. (The samples was/were analyzed on behalf of the applicant as mixing sample in one testing. The above results was/were only given as the informality value.)

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PFOS 参考資訊(Reference Information): 指令 2006/122/EC (Directive 2006/122/EC)

- (1) 該物質不可置於市場上或使用於特殊物質或配置成分重量濃度等於或大於0.005%. (May not be placed on the market or used as a substance or constituent of preparations in a concentration equal to or higher than 0.005% by mass.)
- (2) 該物質不可置於市場上的半成品或商品或其物件;假若零件上明顯地具有PFOS並參照結構上及微細構造上計 算PFOS重量濃度等於或大於0.1%,而紡織品或其他覆蓋物質,如果PFOS在覆蓋物質中含量等於或大於1µg/m².

(May not be placed on the market in semi-finished products or articles, or parts thereof, if the concentration of PFOS is equal to or higher than 0.1% by mass calculated with reference to the mass of structurally or microstructurally distinct parts that contain PFOS or, for textiles or other coated materials, if the amount of PFOS is equal to or higher than 1µg/m2 of the coated material.)

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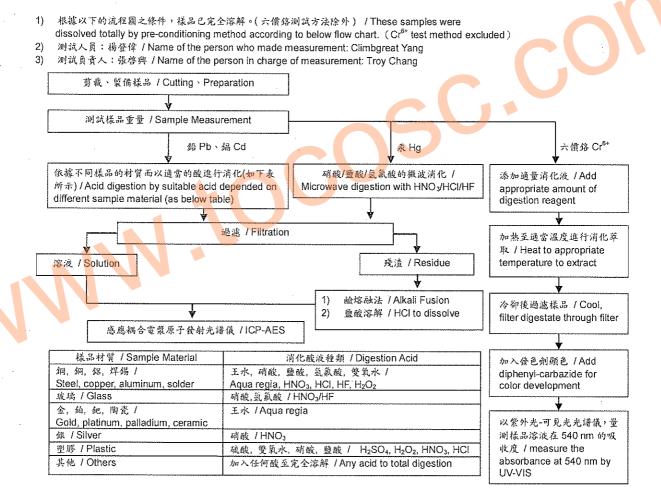
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索式萃取分析流程圖 /

Analytical flow chart of Soxhlet extraction (LC/MS) procedure

- 測試人員:翁賜彬 / Name of the person who made measurement: Roman Wong
- 測試負責人:張啓興 / Name of the person in charge of measurement: Troy Chang

【測試項目:全氟辛烷磺酸/全氟辛酸(銨)、苯並三唑類化合物/

Test Items: PFOS/PFOA · Benzotriazole)

樣品前處理/分樣 / Sample pretreatment/separation

樣品萃取採用索式萃取法 / Sample extraction by soxhlet method

萃取液濃縮/稀釋 / Concentrate/Dilute Extracted solution

以液相層析質譜儀分析萃取液 / Analysis was performed by LC/MS

数據 / Data

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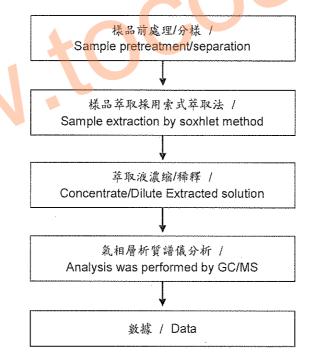
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索式萃取分析流程圖 / Analytical flow chart of Soxhlet extraction (GC/MS) procedure

- 测試人員:翁賜彬 / Name of the person who made measurement: Ro<mark>m</mark>an Won<mark>g</mark>
- 测試負責人:張啓興 / Name of the person in charge of measurement: Troy Ch<mark>a</mark>ng

【測試項目:可塑劑、苯並三唑類化合物、六溴環十二烷、壬酚、單甲基二溴二苯基甲烷、有機磷化合物 / Test Items: Phthalate、Benzotriazole、HBCDD、NP、DBBT、Organic phosphorus compounds 】



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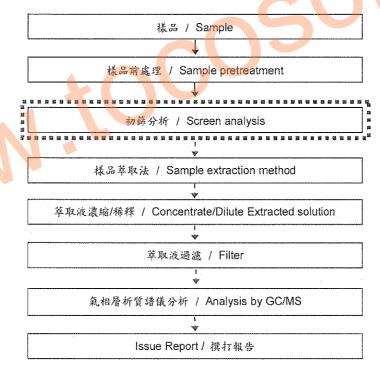
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分析流程圖 / Analytical flow chart

- 测試人員:翁賜彬 / Name of the person who made measurement: Roman Wong
- 脚 測試負責人:張啓與 / Name of the person in charge of measurement: Troy Chang
 【測試項目(Test Items): 多溴聯苯/多溴聯苯醚、四溴雙酚-A-雙 / PBB/PBDE, TBBP-A-bis】

初次测试程序 / First testing process — 🍑 選择性篩檢程序 / Optional screen process \cdots - 🔸



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Test Report

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西北臺慶科技股份有限公司 / TAI-TECH ADVANCED ELECTRONICS CO., LTD.

100 March 1997

(東莞臺慶精密電子有限公司 / TAI-TECH ADVANCED ELECTRONICS (DONGGUAN) CO., LTD.)

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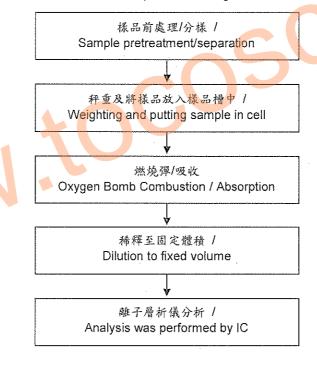
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(廣東省東莞市黄江鎮黄牛埔福祥街2號 / NO. 2, FUXIANG STREET, HUANGNIUPU, HUANGJIANG TOWN, DONGGUAN, GUANGDONG)

(江蘇省昆山市篷朗昆嘉高科技工業區郭澤路 / GUO-ZE ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA)

鹵素分析流程圖 / Analytical flow chart of halogen content

- 1) 测試人員:陳恩臻 / Name of the person who made measurement: Rita Chen
- 2) 測試負責人:張啓興 / Name of the person in charge of measurement: Troy Chang



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* 照片中如有箭頭標示,則表示爲實際檢測之樣品/部位. *

(The tested sample / part is marked by an arrow if it's shown on the photo.)

CE/2011/C1821

** 報告結尾 (End of Report) **

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